

# MS-7B38 Ver:1.1

**CPU:**  
AMD AM4

**System Chipset:**  
Promontory A320  
(Value DIY or System Builder)

**Main Memory:**  
DDR IV \* 2 MAX:64 GB

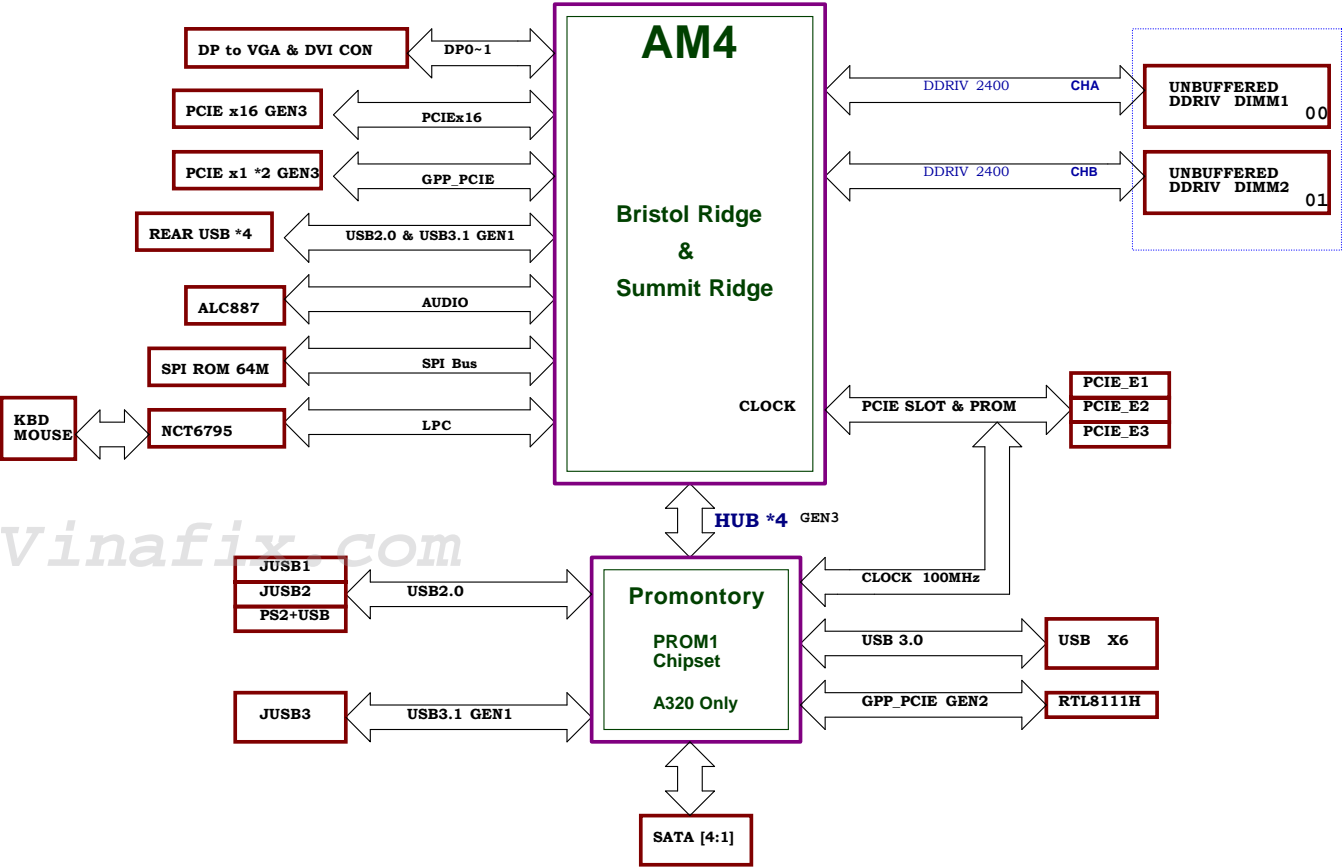
**VRM**  
RT8894 3+2

**On Board Chipset:**  
LPC Super I/O --NCT6795  
LAN RTL8111H  
Azalia CODEC - Realtek ALC887

**Expansion Slots:**  
From CPU  
PCI Express X16 Slot \* 1  
PCI Express X1 Slot \* 1  
PCI Express X1 Slot \* 1


**OCP IC:**  
UP6273

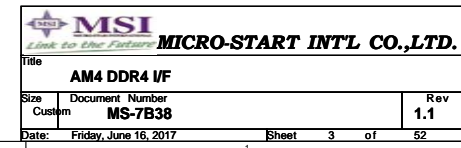
## FUSION BLOCK DIAGRAM



# AMD AM4

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34 DDR Power-RT8231AGQW	
35 CPU Power 1P8V-MP2147	

 <b>MICRO-START INT'L CO.,LTD.</b>		
Title <b>COVER SHEET</b>		
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Not supported PCIe on AMD Family 15h Models 60h-6Fh

Only supported on AMD Family 17h/Models 00h-0Fh

Not supported PCIe on AMD Family 15h Models 60h-6Fh

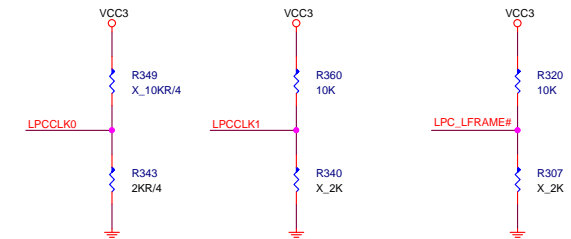
Not supported on AMD Family 15h Models 60h-6Fh

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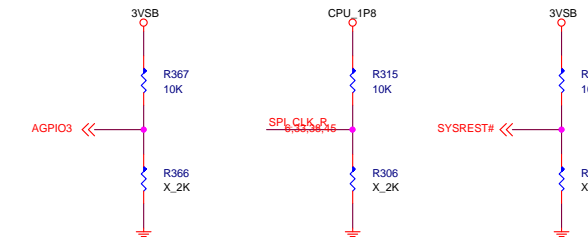




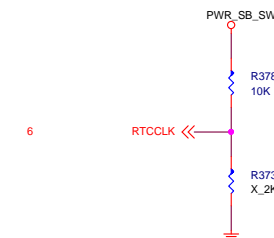
# Strapping Options



	LPCCLK0	LPCCLK1	SIO_LFRAME
PULL HIGH	LPC device Boot Fail Timer Enabled	Configured for Internal clock generator (Default)	SPI ROM (Default)
PULL LOW	LPC device Boot Fail Timer Disabled (Default)	Configured for External clock generator ?????	LPC ROM



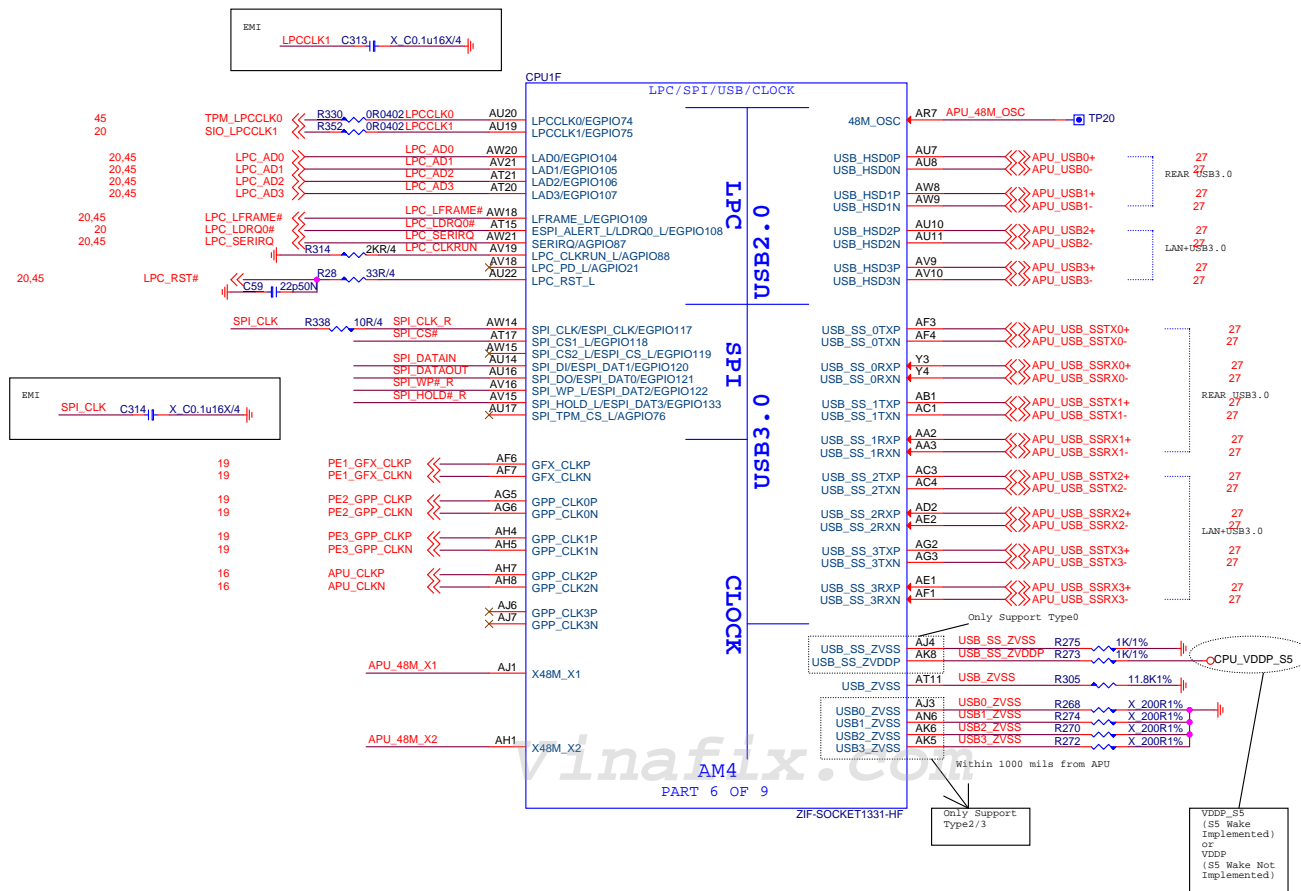
	AGPIO3	SPI_CLK	SYSREST#
PULL HIGH	Enhanced Reset logic (Default)	Use 48Mhz crystal clock and generate both internal and external clocks (Default)	Normal reset mode (Default)
PULL LOW	Traditional Reset logic	Use 100Mhz PCIE clock as reference clock and generate internal clocks only	short reset mode



	RTCCLK
PULL HIGH	RTC Coin Battery is on board (Default)
PULL LOW	RTC Coin Battery is not on board



Title	AM4 LPC/SPI/USB/CLK/STRAP		
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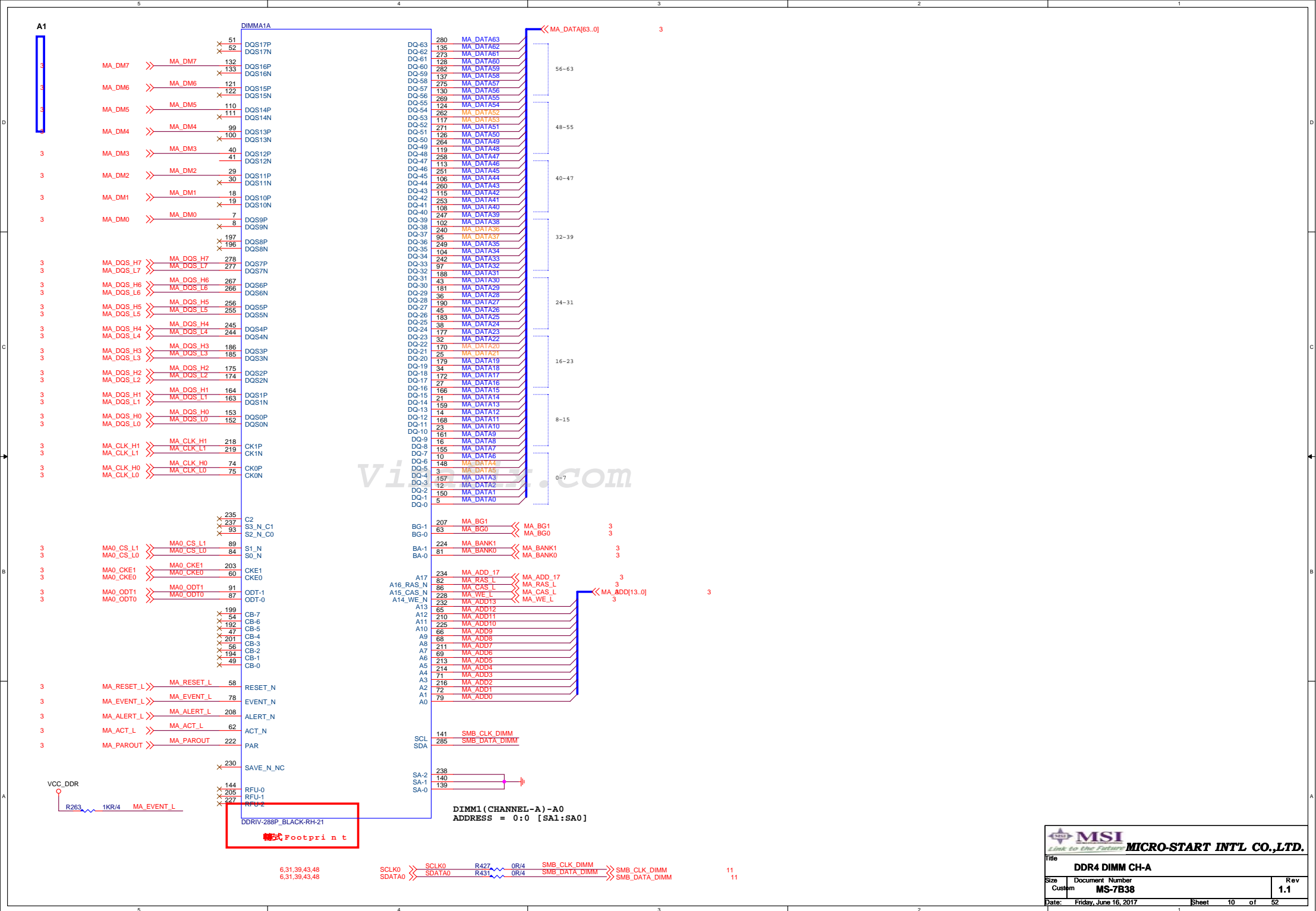


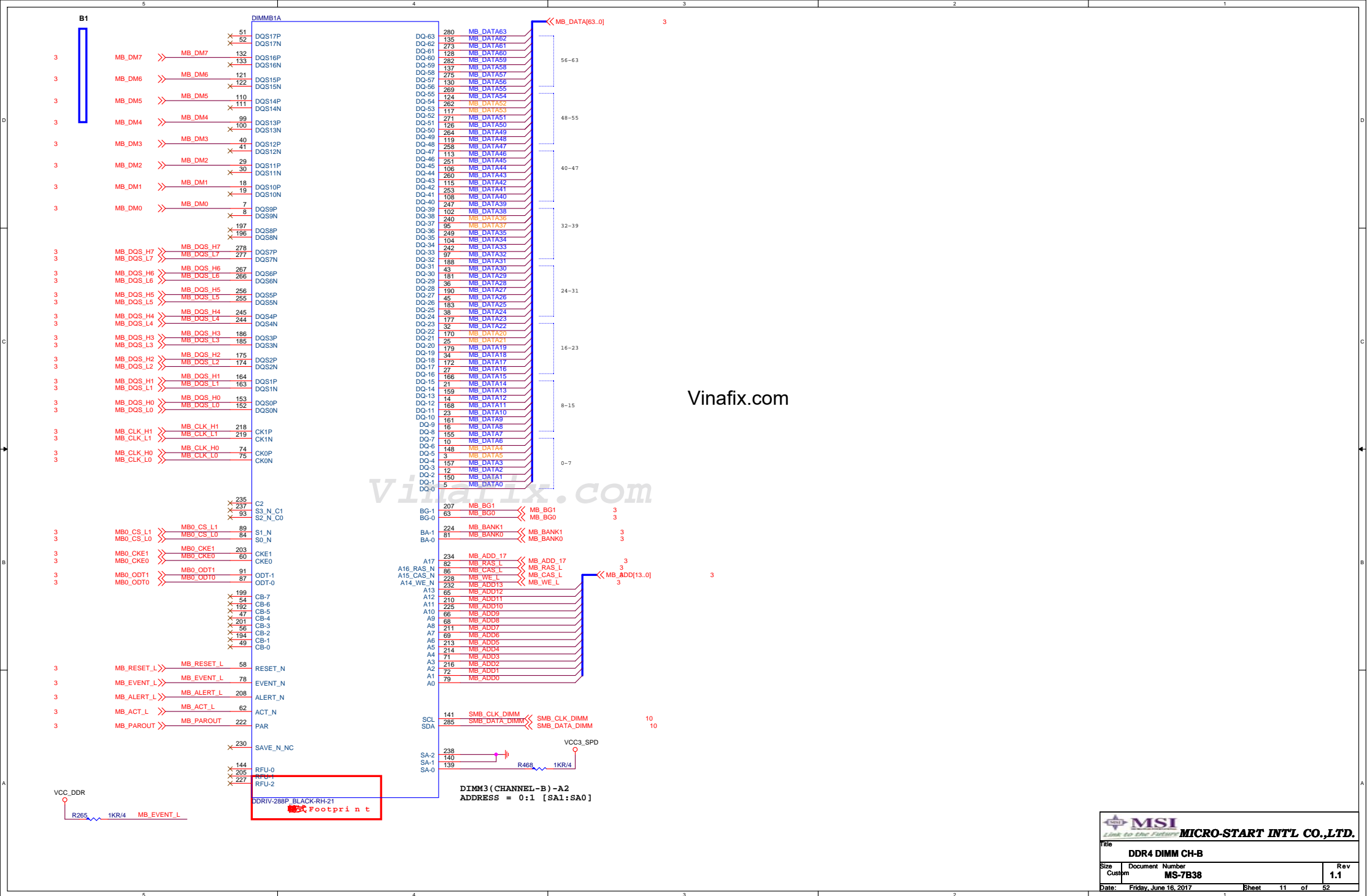


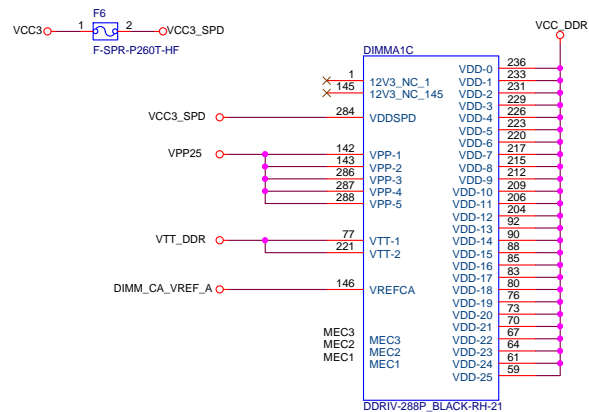
GND

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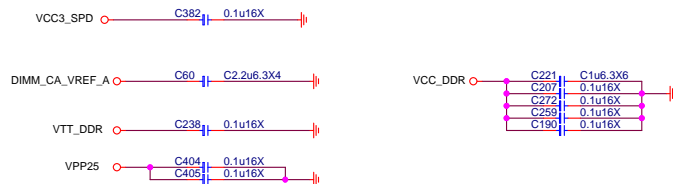
AM4  
PART 9 OF 9



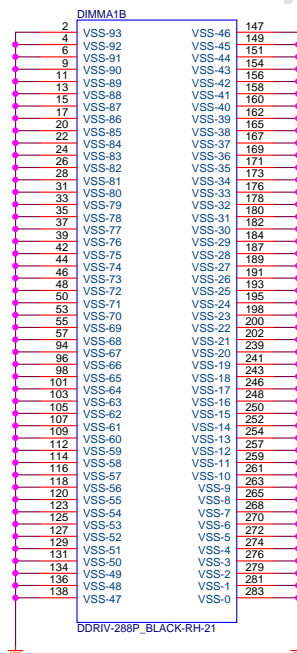




DIMM SLOT PN BY SPEC

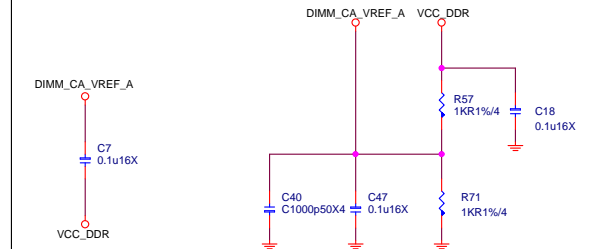


Vinafix.com



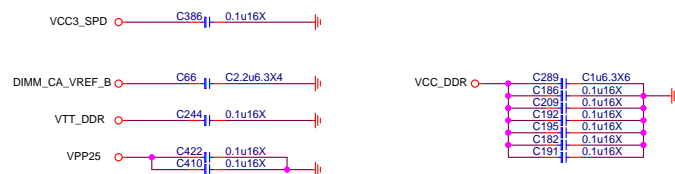
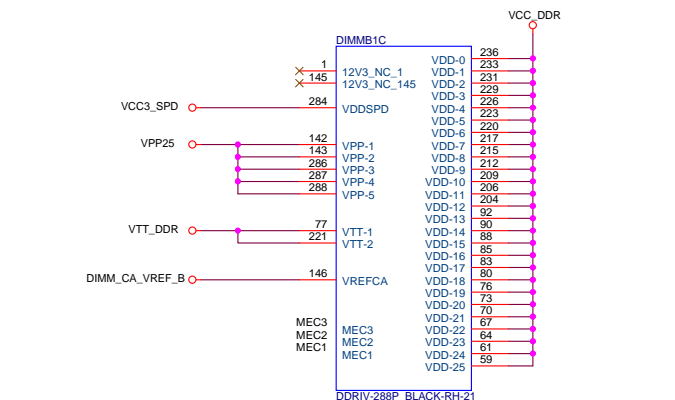
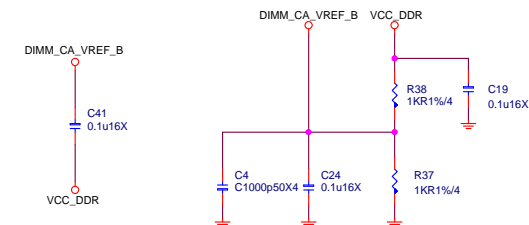
## DDR VREF

(place resistors close to DIMMs)

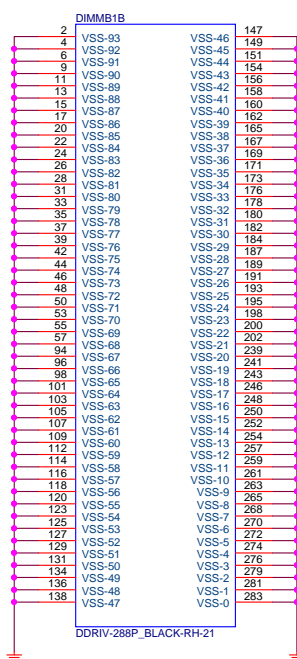


## DDR VREF

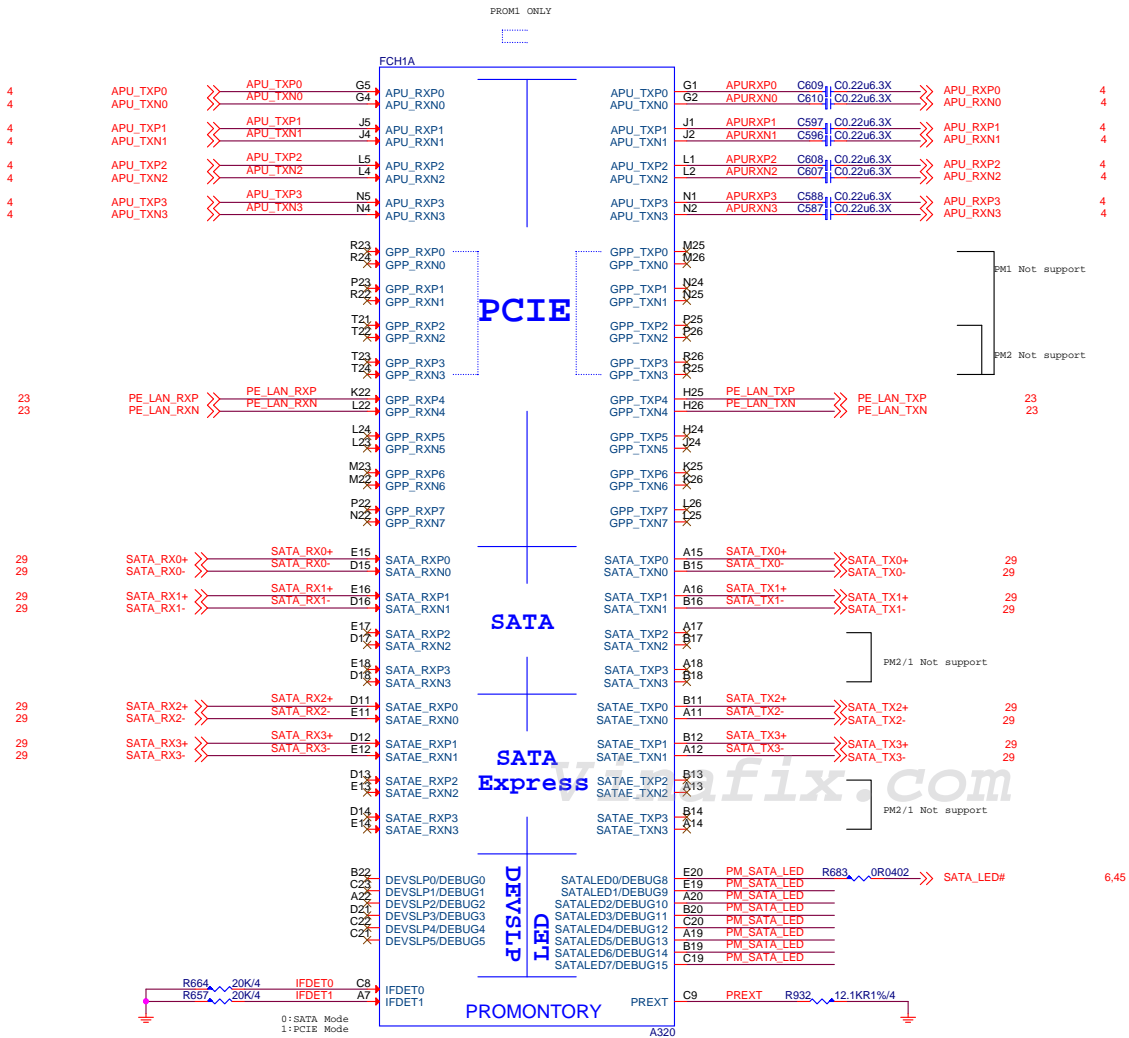
(place resistors close to DIMMs)



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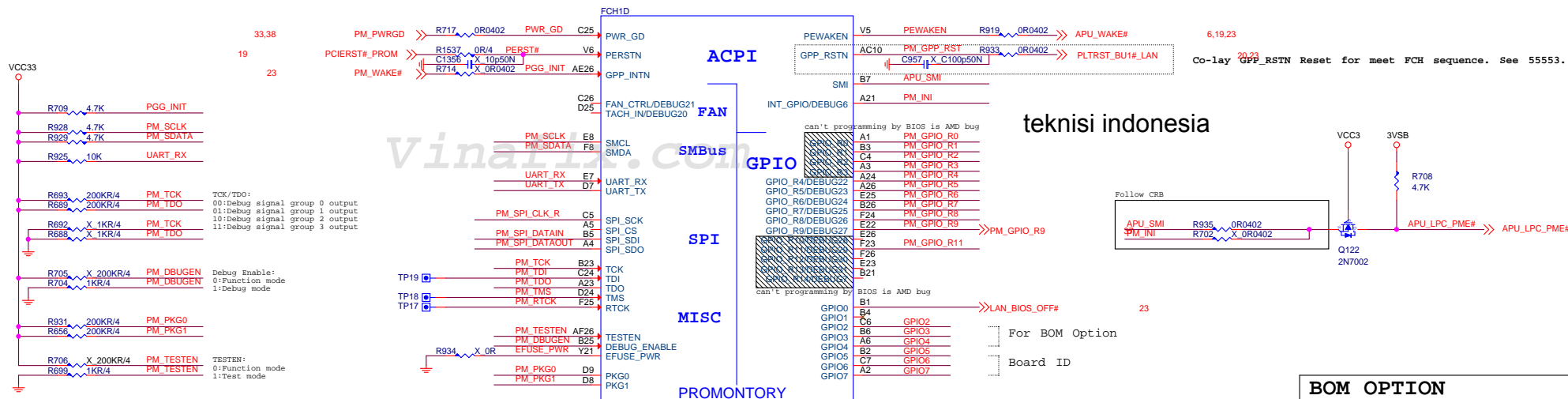
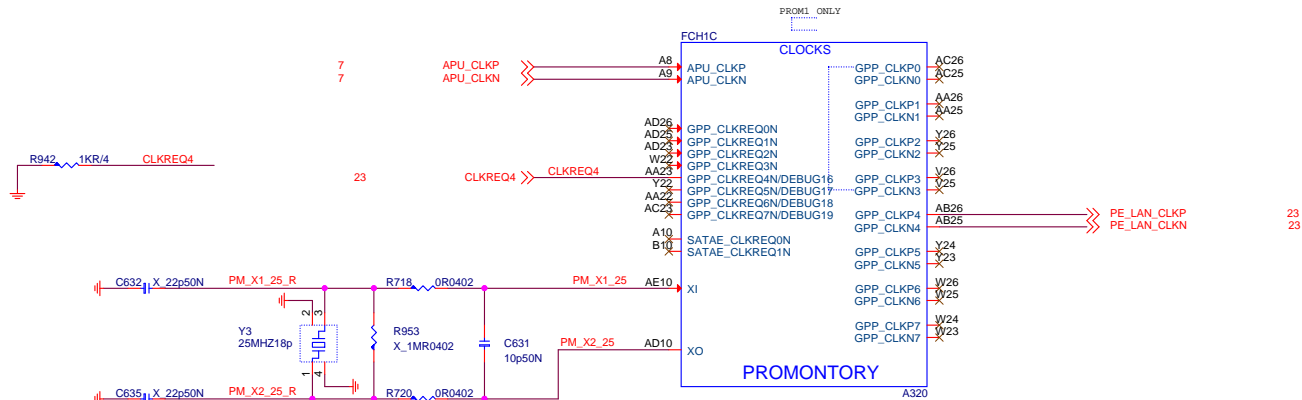


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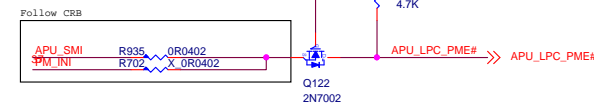




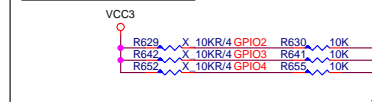




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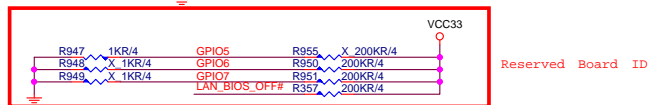
## BOM OPTION

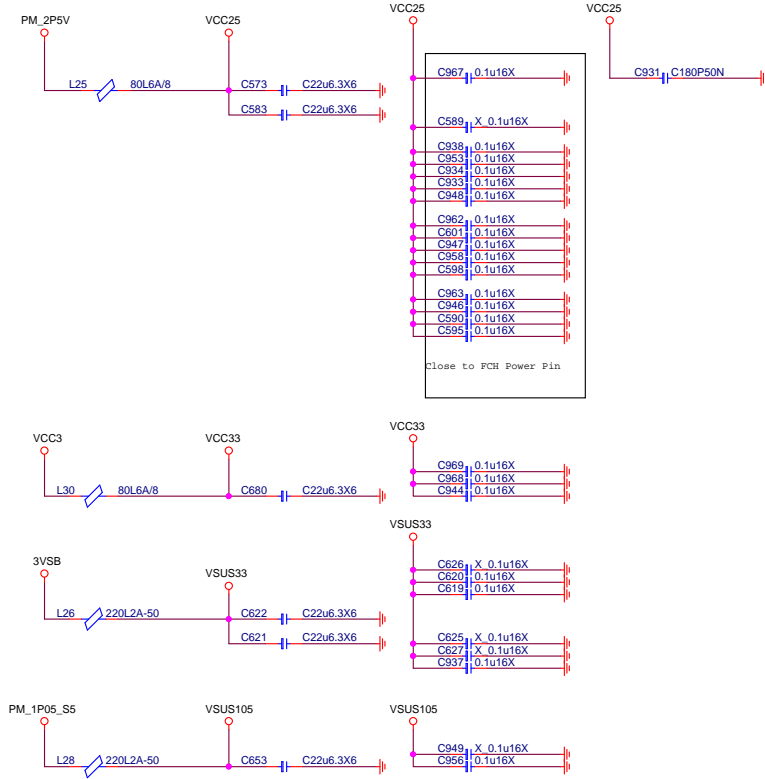
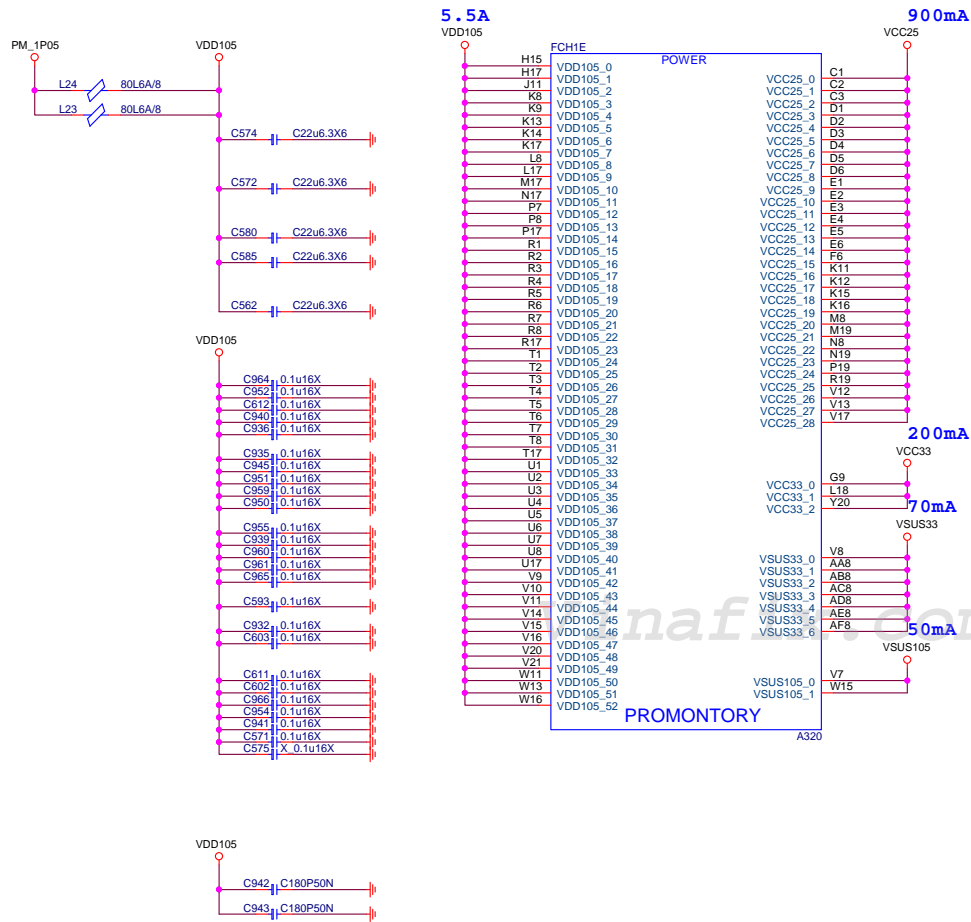


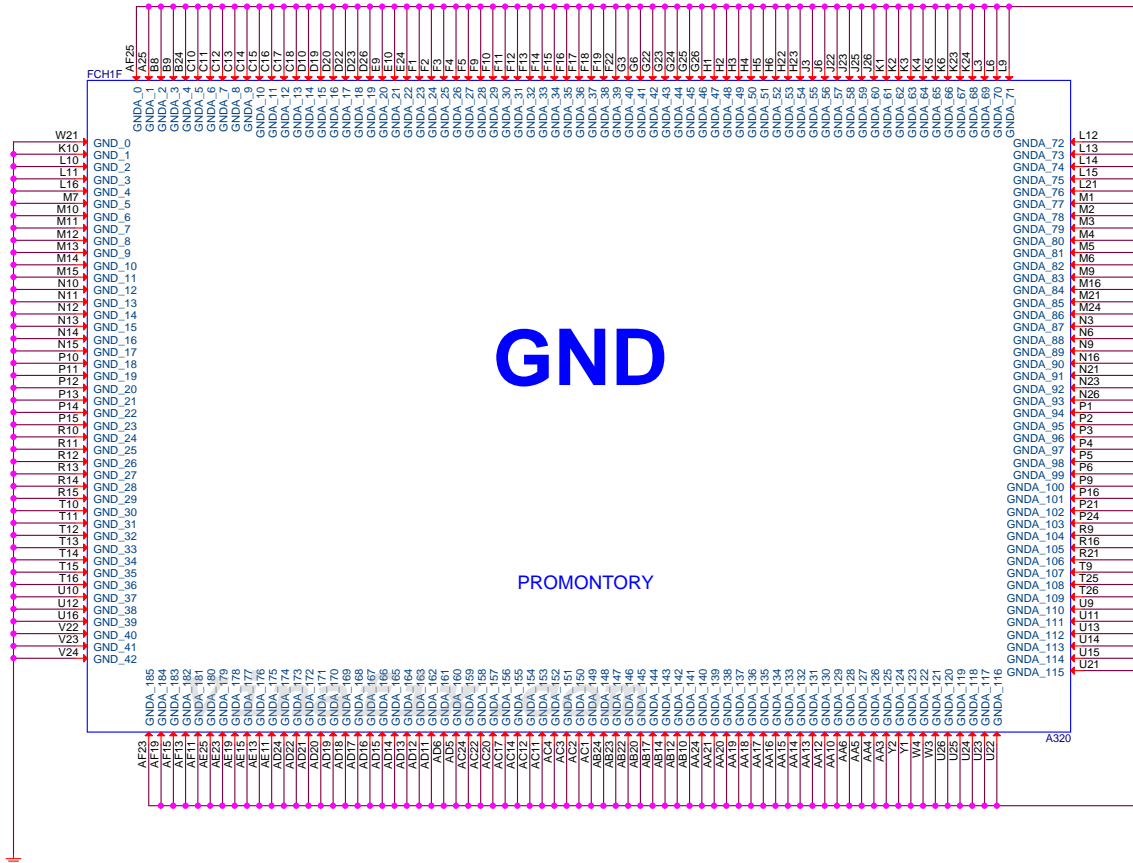
	FULL	
GPIO2	0	
GPIO3	0	
GPIO4	0	

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Title	Promontory-CLK/ACPI/GPIO	
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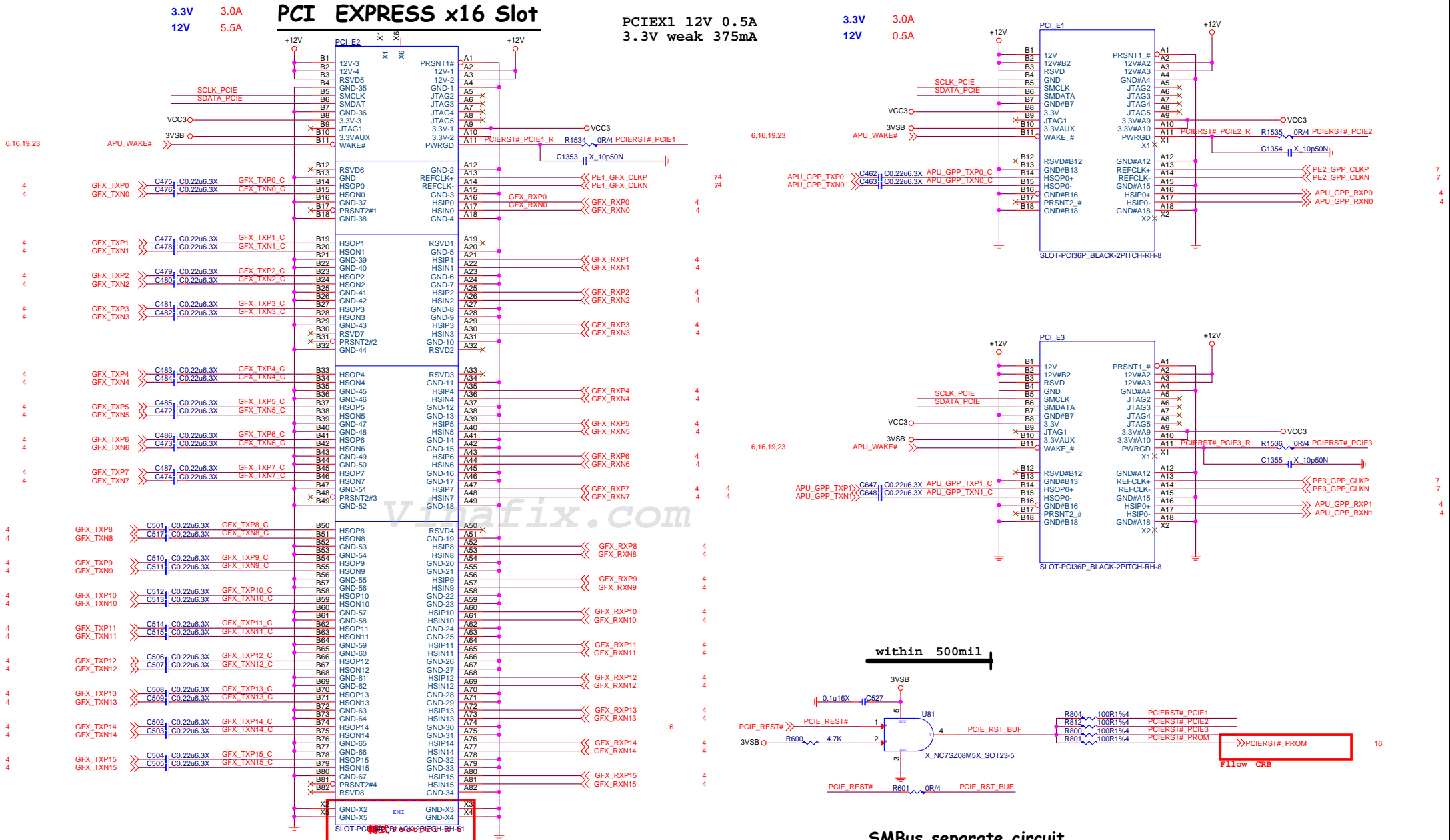




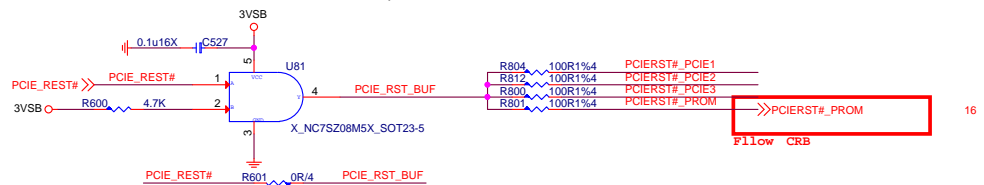
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# PCI EXPRESS x16 Slot

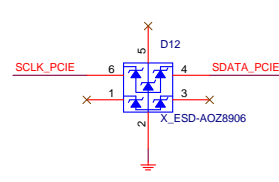
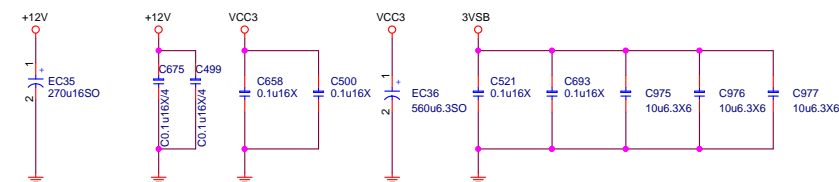
PCIEX1 12V 0.5A  
3.3V weak 375mA



within 500mil

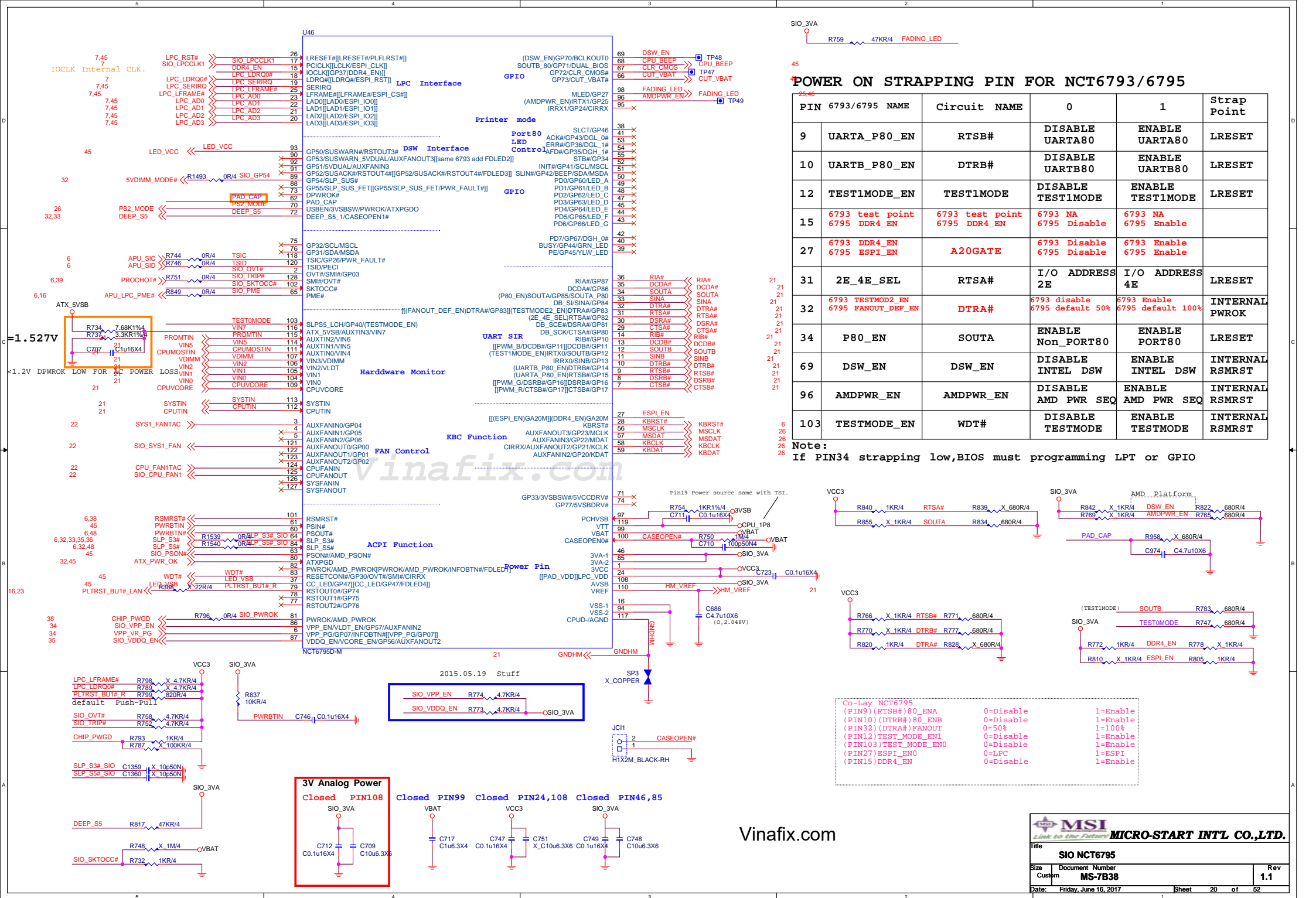


## SMBus separate circuit



SMB\_SEL  
GPIO Default High

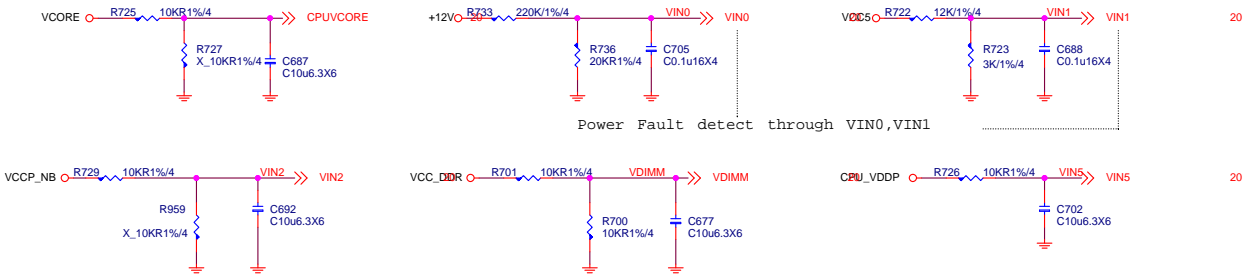
Link to the Future		
MICRO-START INTL CO.,LTD.		
Title		
PCIE X16(X1*2) SLOT		
Size	Document Number	Rev
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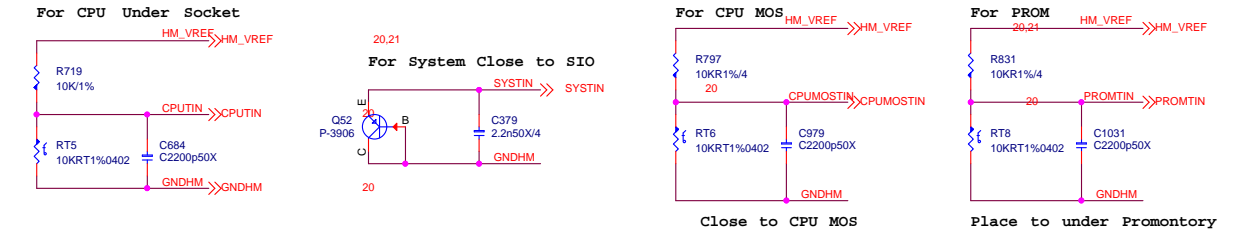


HW Monitor - Voltage

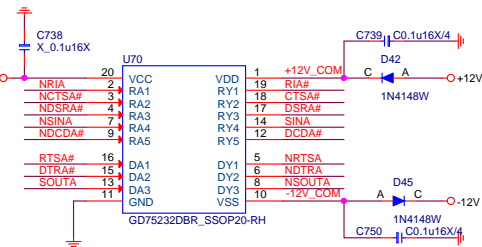
SIO HM Voltage over 2.048V will not detect



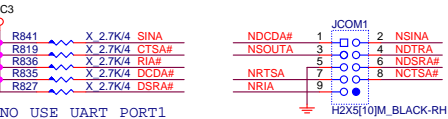
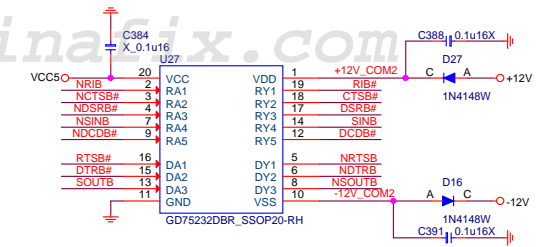
Thermal Monitor



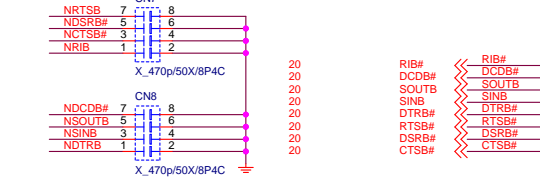
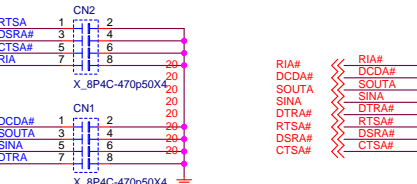
SERIAL PORT 1



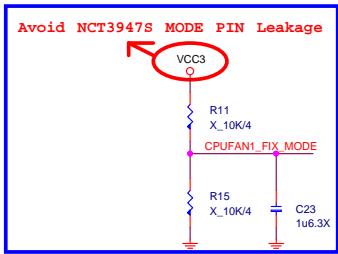
SERIAL PORT 2



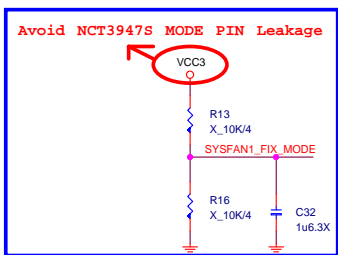
NO USE UART PORT1



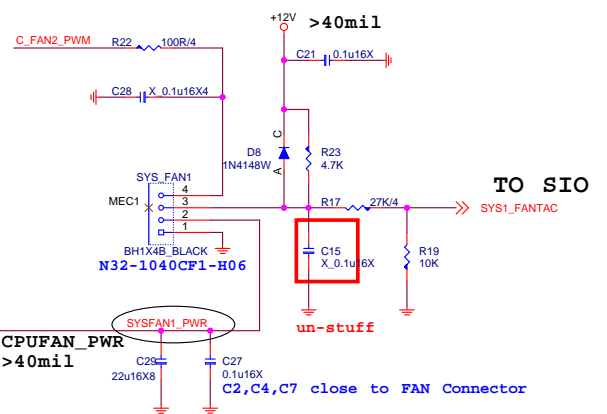
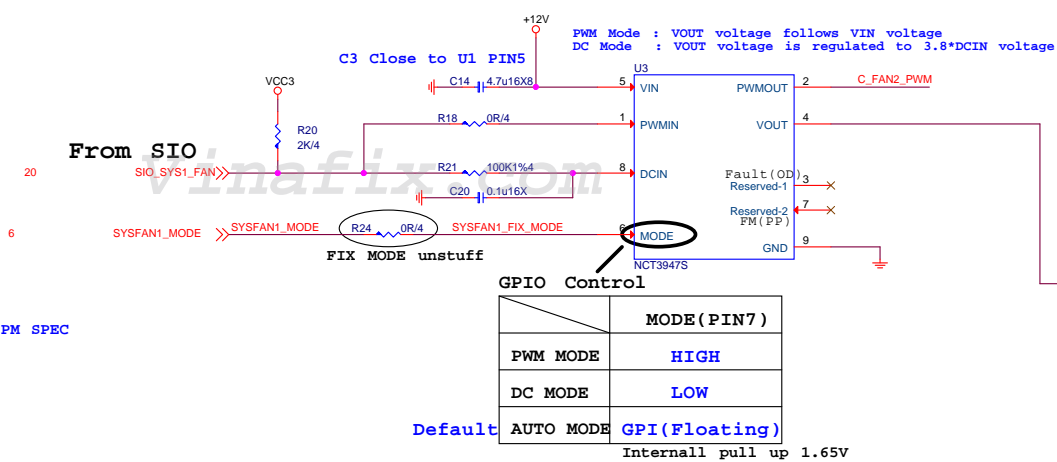
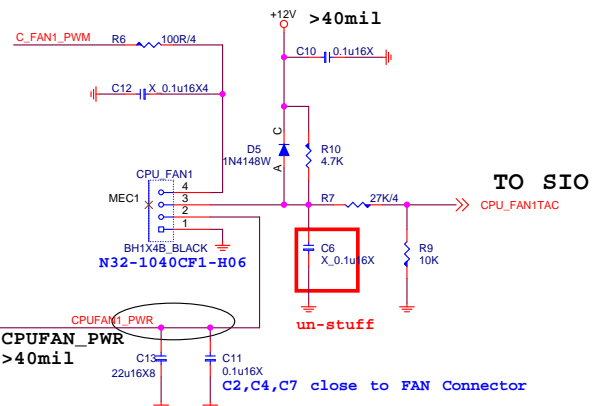
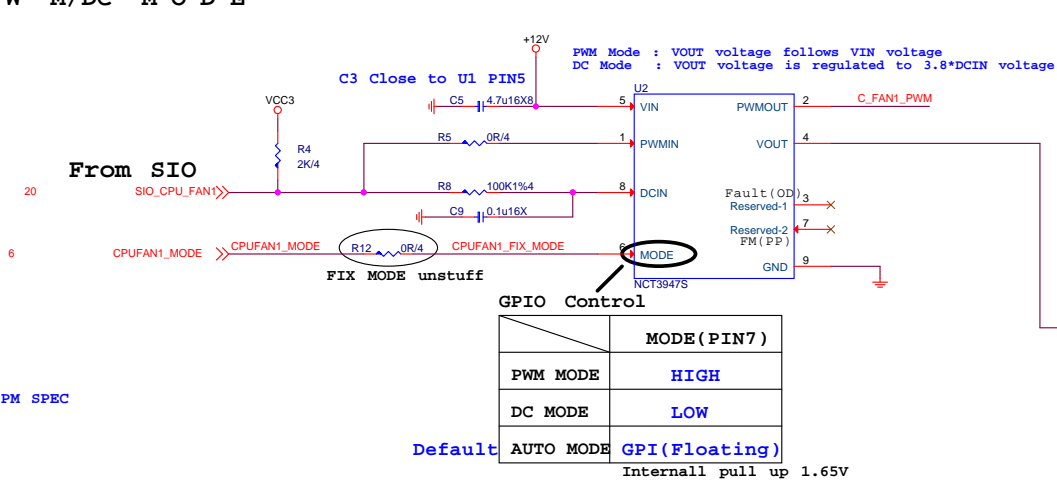
TYPE K : 4 PIN CPU FAN USE NCT3947S USE PCH GPIO CONTROL FAN MODE  
2.GPIO 由 B0 切换 PW M/DC M O D E



Resever For FIX DC or PWM MODE USE By PM SPEC

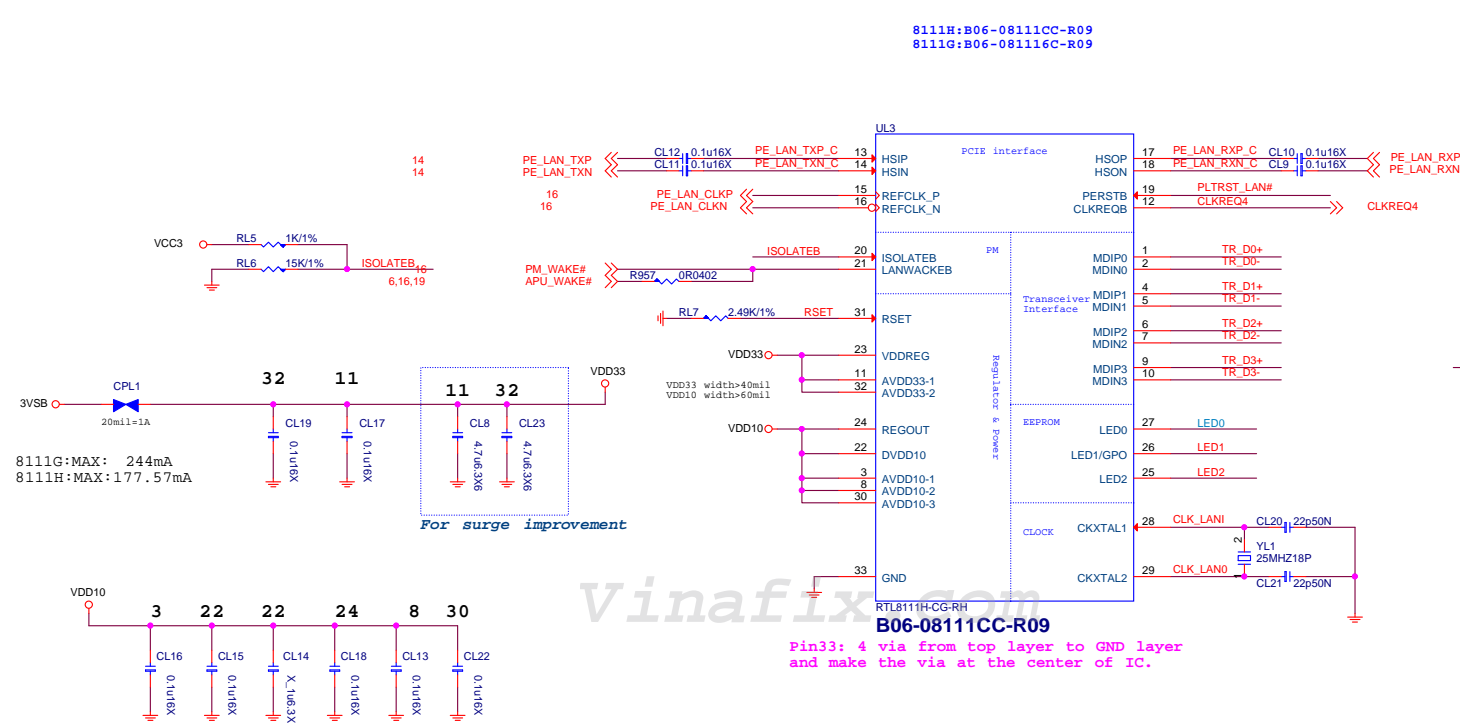


Resever For FIX DC or PWM MODE USE By PM SPEC



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RTL8111G/RTL8111H Giga LAN



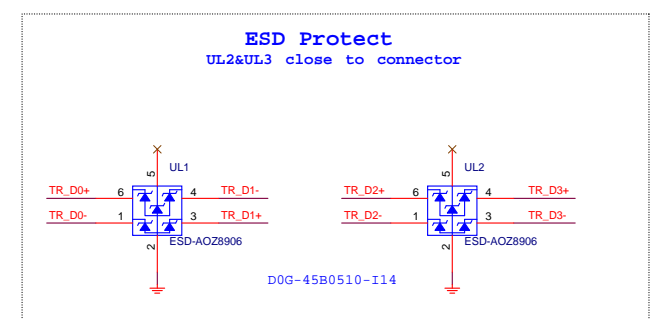
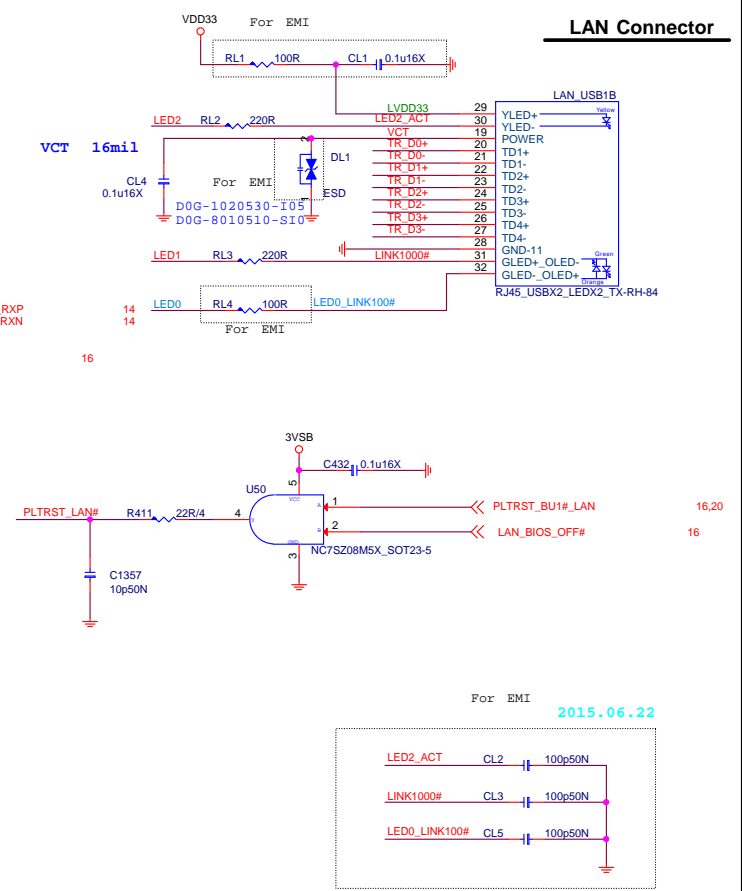
8111G:MAX: 244mA  
8111H:MAX:177.57mA

8111G POWER Consumption

	3.3V @ mA	mW
10 M Idle/TxRx	17.15/116.7	56.6/385.1
100 M Idle/TxRx	71.45/129.5	235.8/427.4
Giga Idle/TxRx	179.1/243.9	591/804.9
ALDPS	6.41	21.15

8111H POWER Consumption

	3.3V @ mA	mW
10 M Idle/TxRx	9.9/84.69	32.67/279.48
100 M Idle/TxRx	48.11/92.44	158.76/305.05
Giga Idle/TxRx	124.5/177.57	410.85/585.98
ALDPS	5.50	18.15



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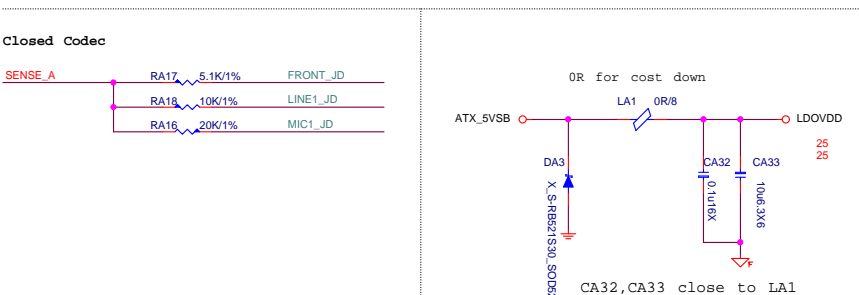
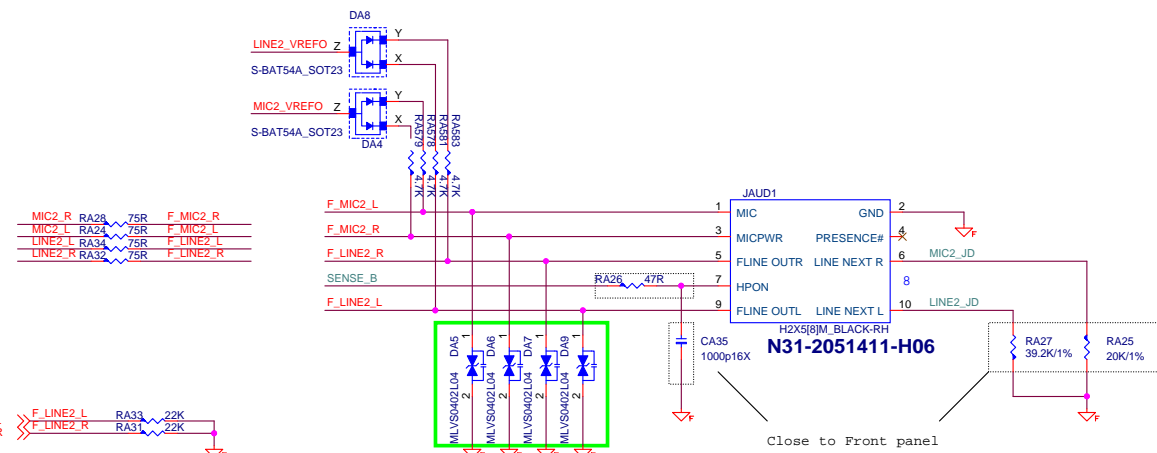
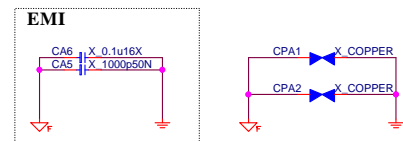
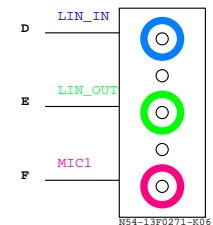
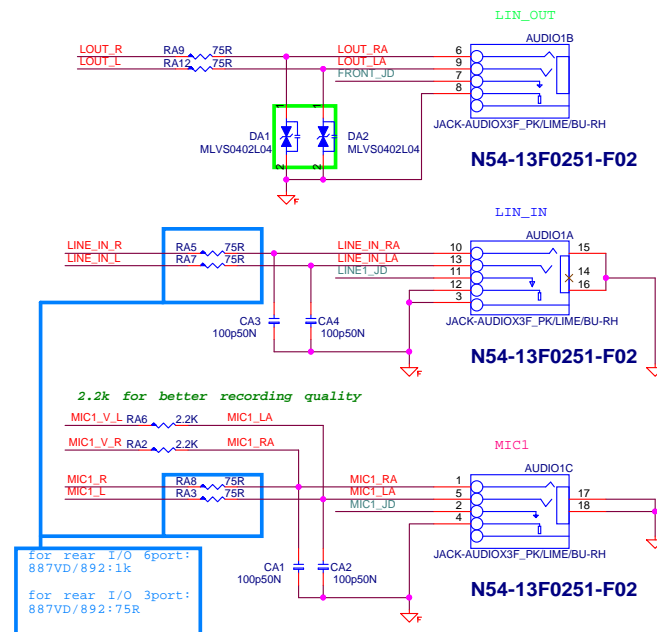
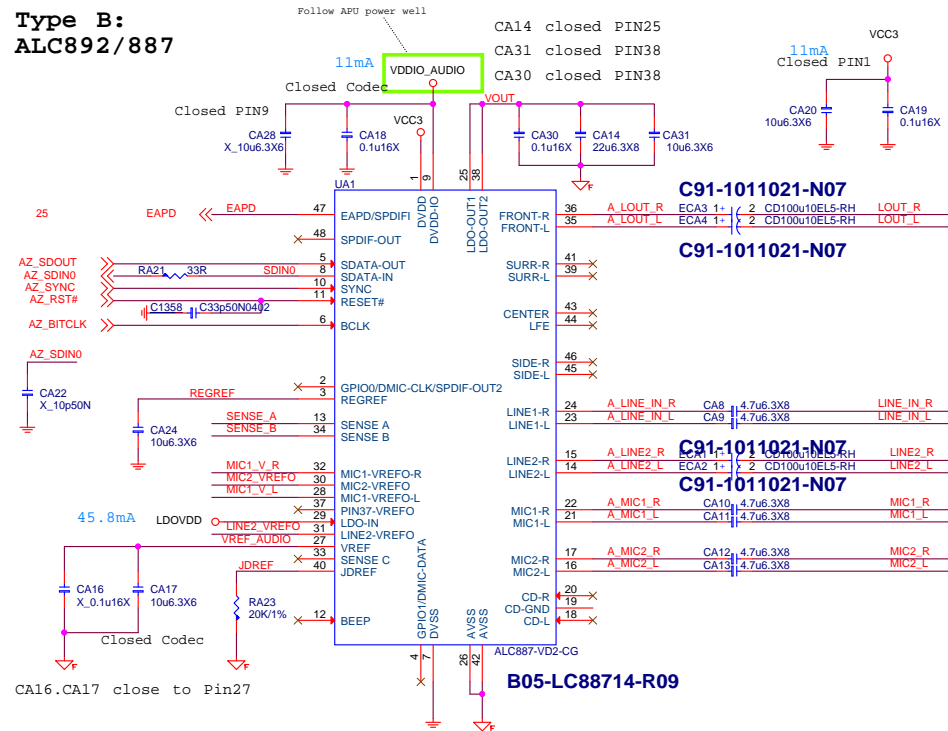
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Size: Custom  
Document Number: MS-7B38  
Date: Friday, June 16, 2017

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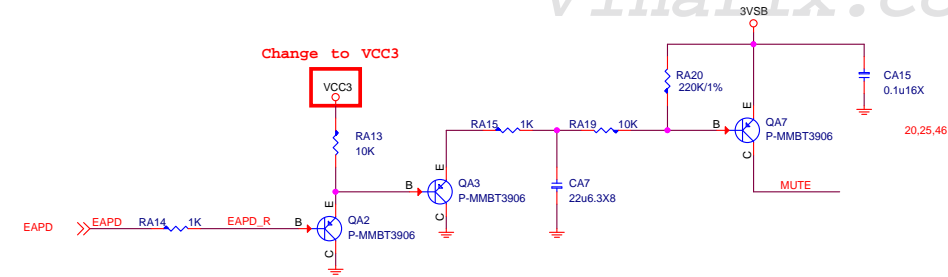
Type B:  
ALC892/887



Varister --> cap for cost down  
D0G-2710510-I05  
D0G-2950500-SI0  
Close to Jack

### Rear Line OUT De-POP circuit

De-pop circuit for Rear Line out & Front Headphone out)

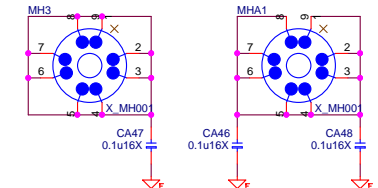
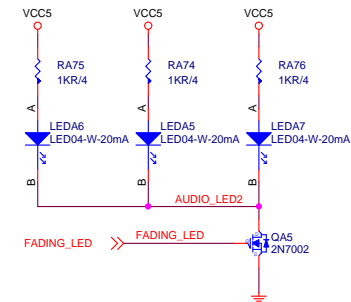
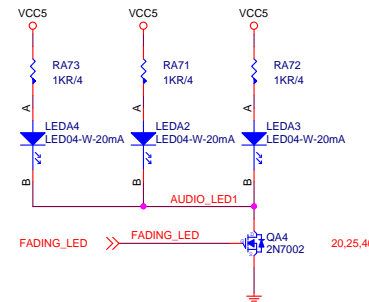


Digital

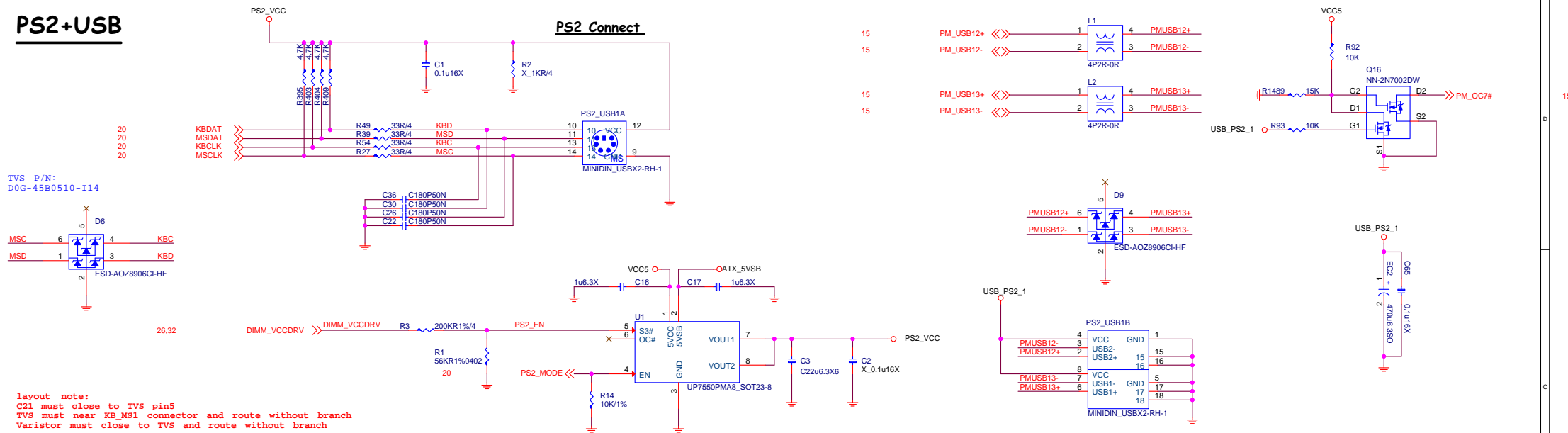
Analog



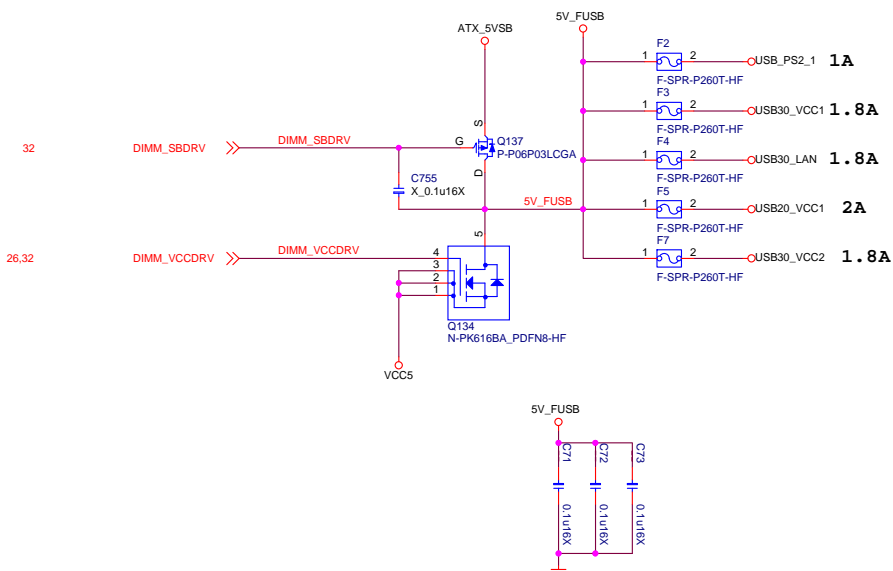
使 用 光 LED



**PS2+USB**

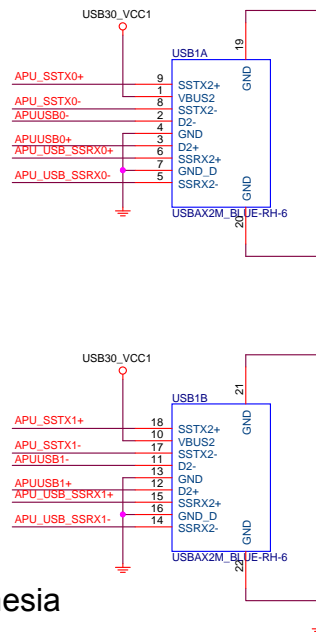
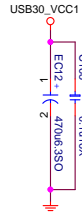
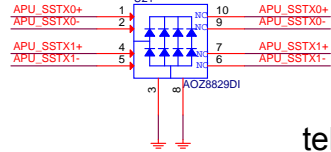
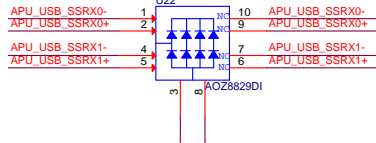
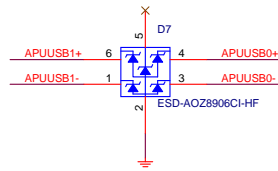
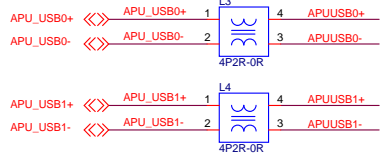
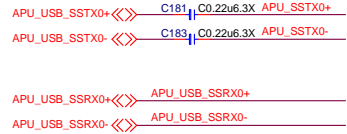


## USB Power



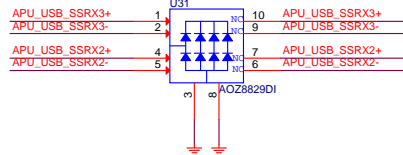
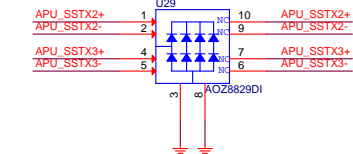
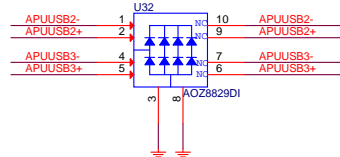
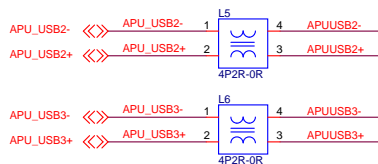


## USB 3.0

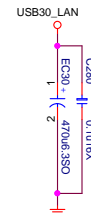
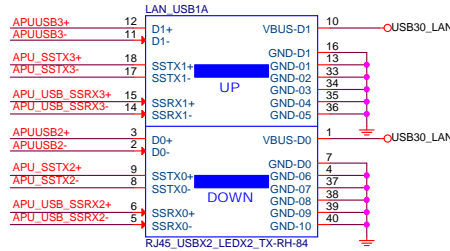


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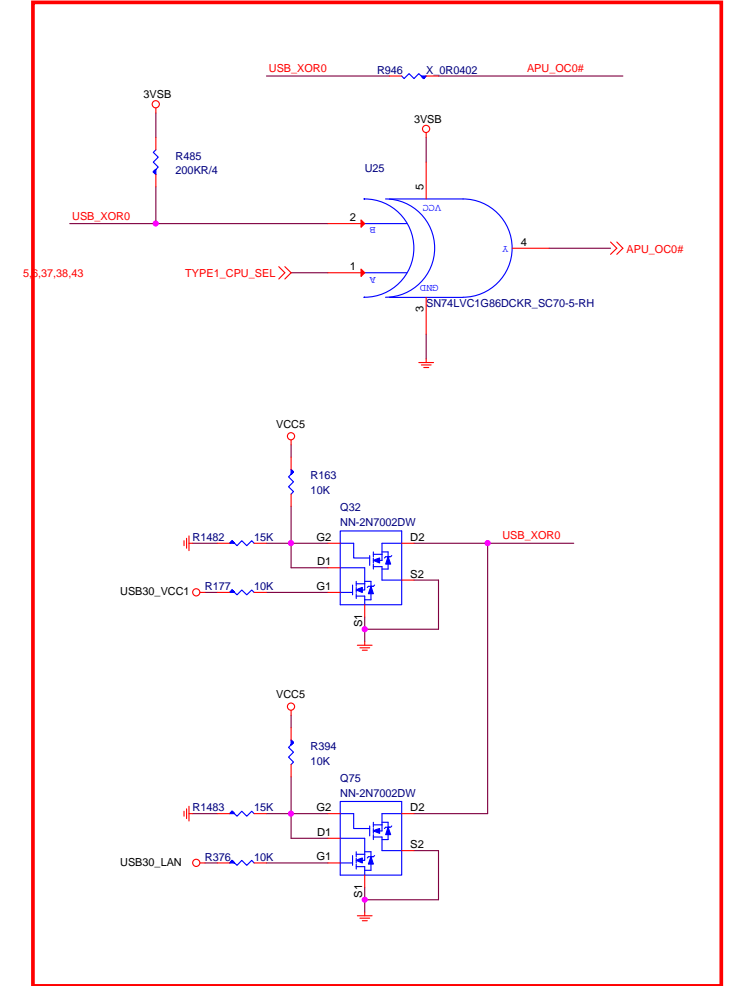
## USB3.1 GEN1



## LAN+USB

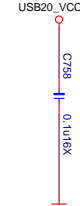
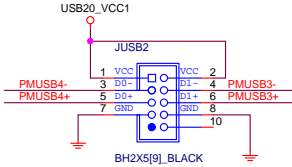
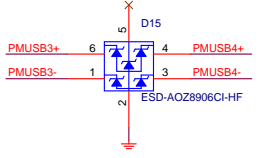
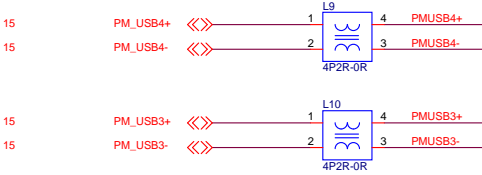
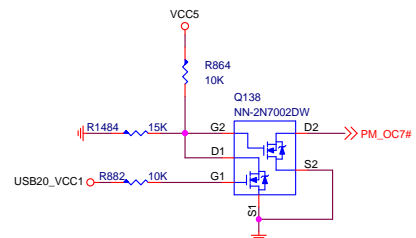
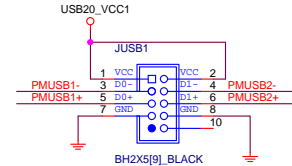
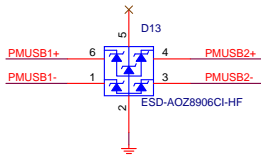
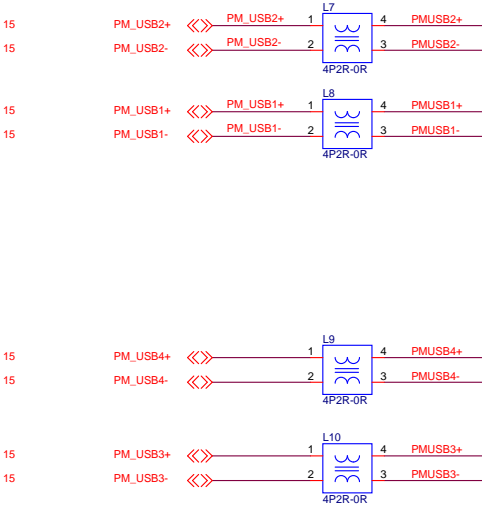


Modify USB\_OC# circuit



	CORETYPE1(A)	USB_PWR(B)	APU_USB_OC(Y)
BR	0	0	0
Act. Low	0	1	1
SR	1	0	1
Act. High	1	1	0

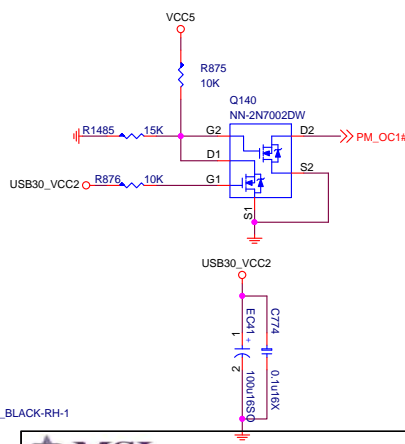
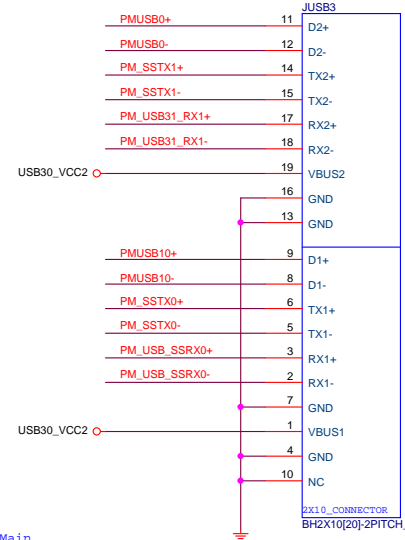
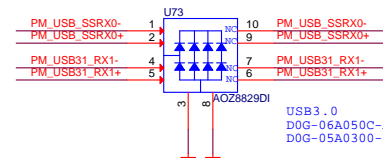
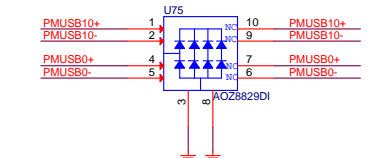
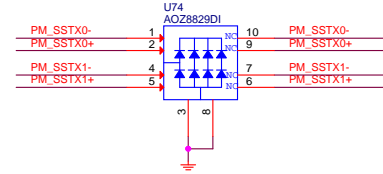
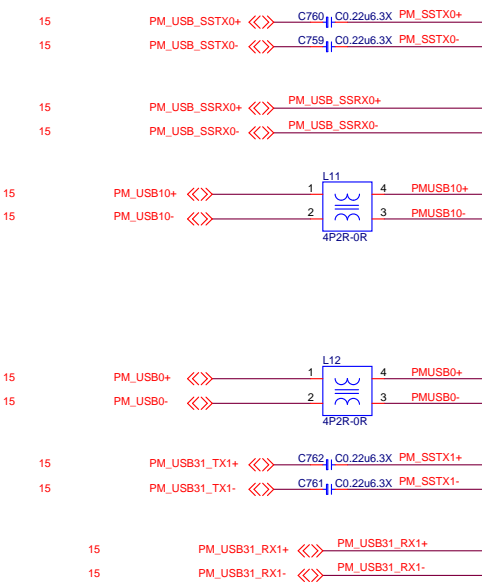
Front USB2.0



Front USB3.1 GEN1

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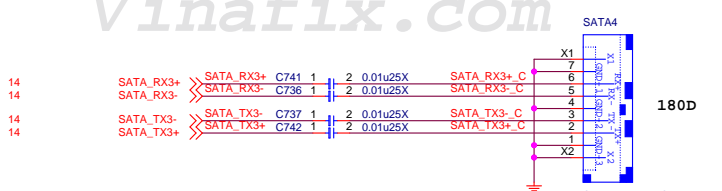
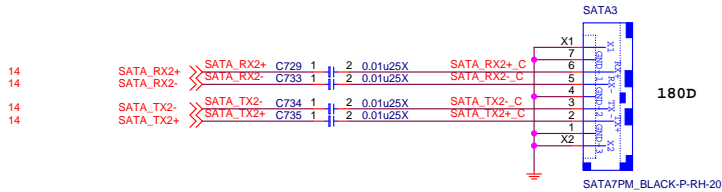
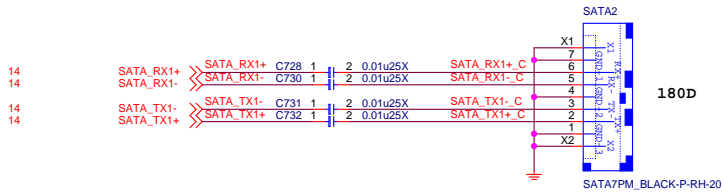
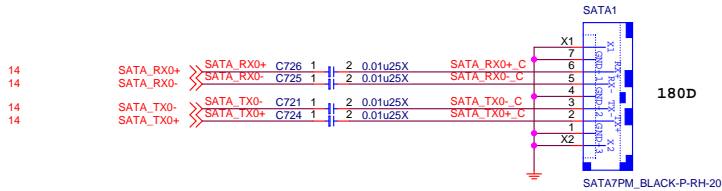
Vinafix.com



USB3.0  
D0G-06A050C-A68 Main  
D0G-05A0300-I14 AVL

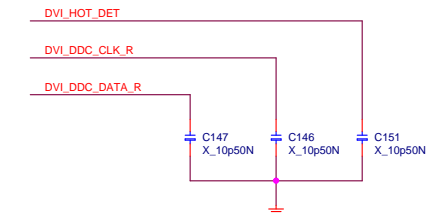
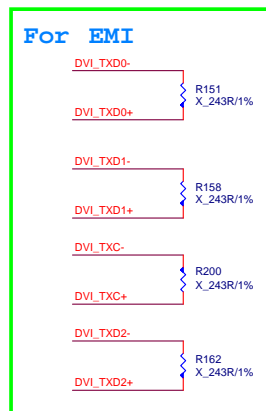
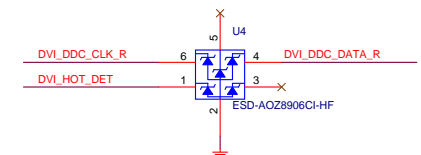
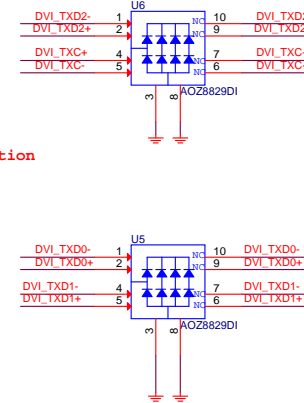
USB2.0  
D0G-0200529-A68 Main  
D0G-0100619-I05 AVL

SATA Connector



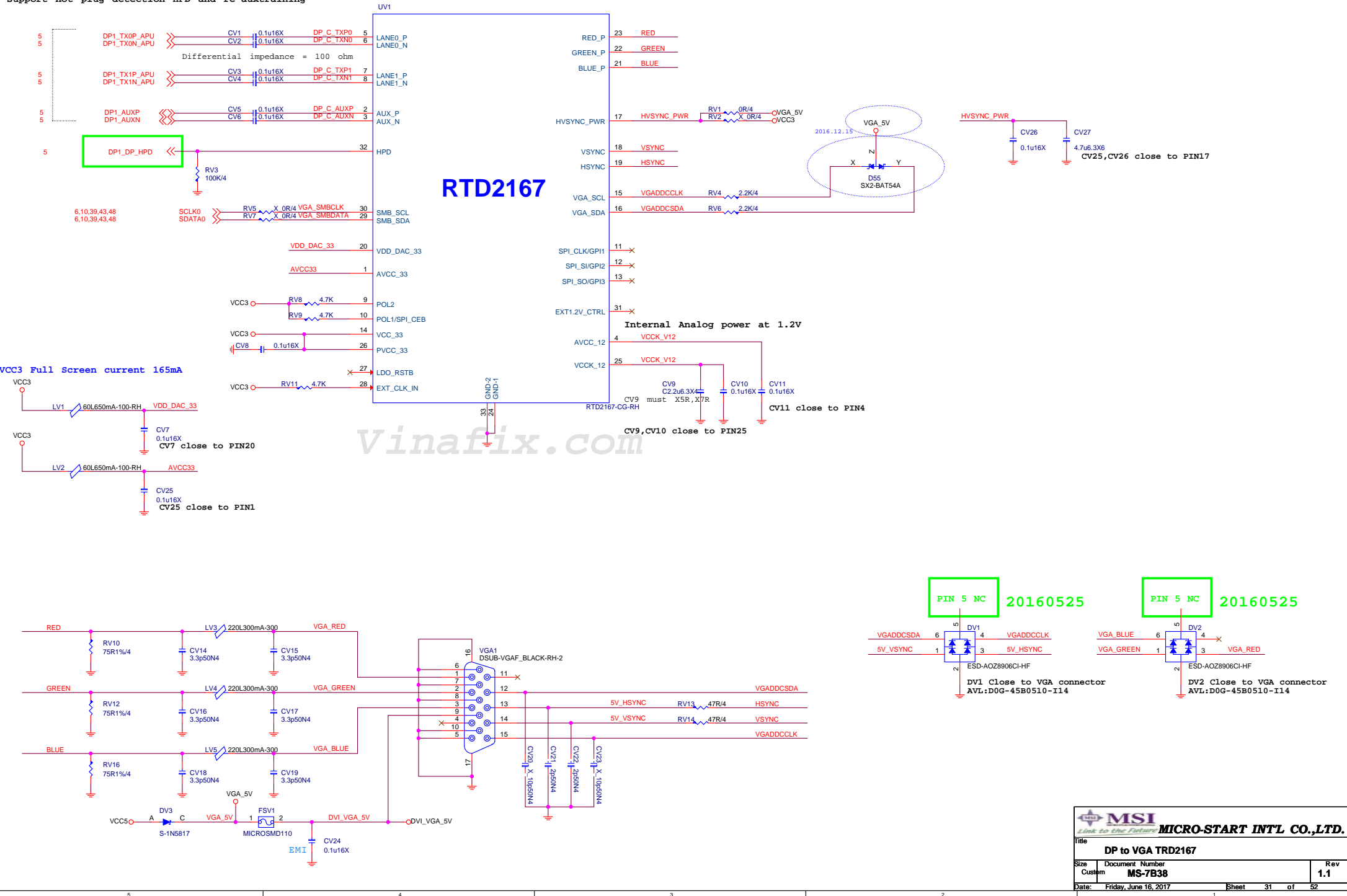
Vinafix.com

VGA: resolution of 2048x1536 pixels with 32-bit color at 75 Hz (4:3 QXGA)

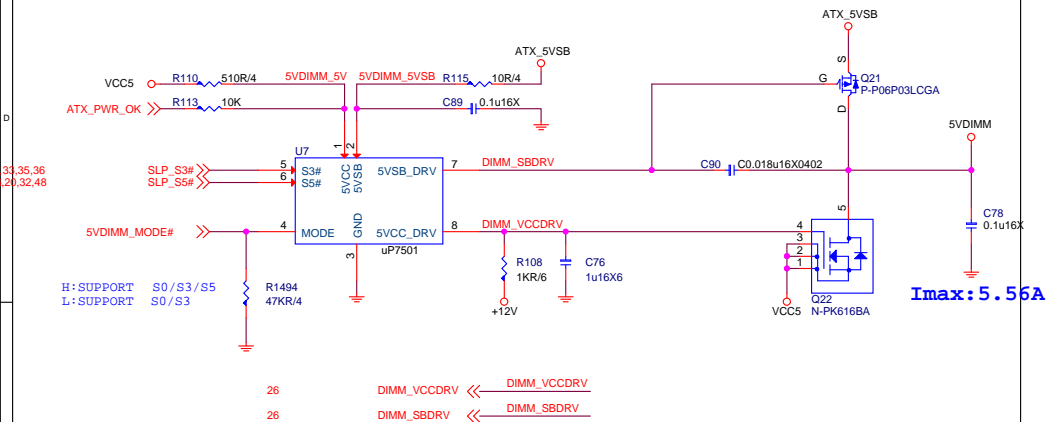


Note:

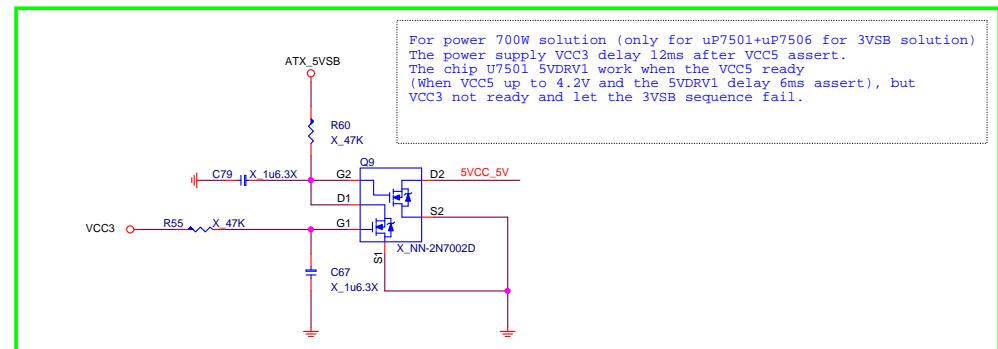
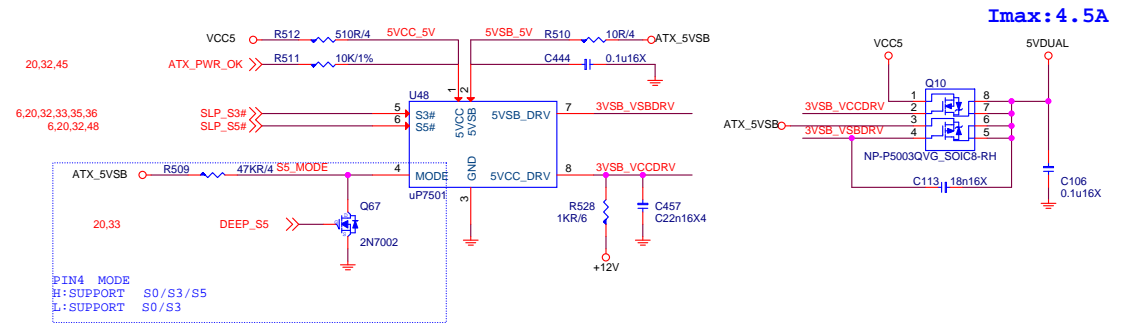
If connect to eDP port,must confirm whether it support hot plug detection HPD and re-auxtraining



## 5VDIMM FOR DDR

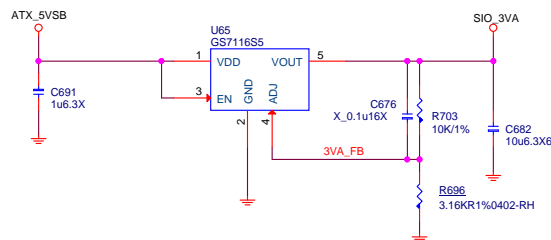


## 5VDUAL For 3VSB CPU 1.8V VDD

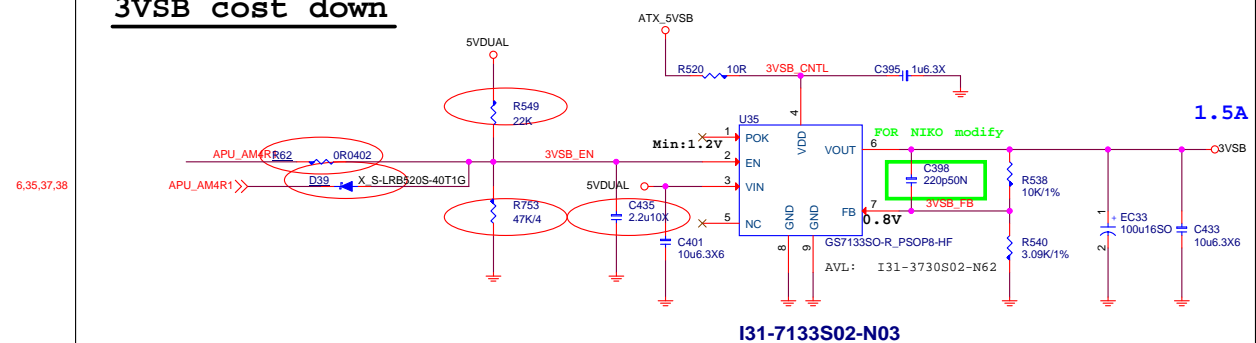


Vinafix.com

## SIO\_3VA

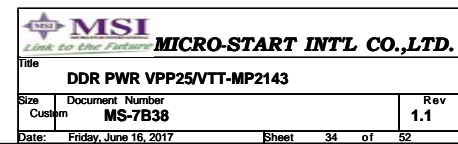


## 3VSB cost down





*Vinafix.com*





DDR4\_1.2V 15.5A+4.75A+0.6A=20.85A

15.5A FOR CPU

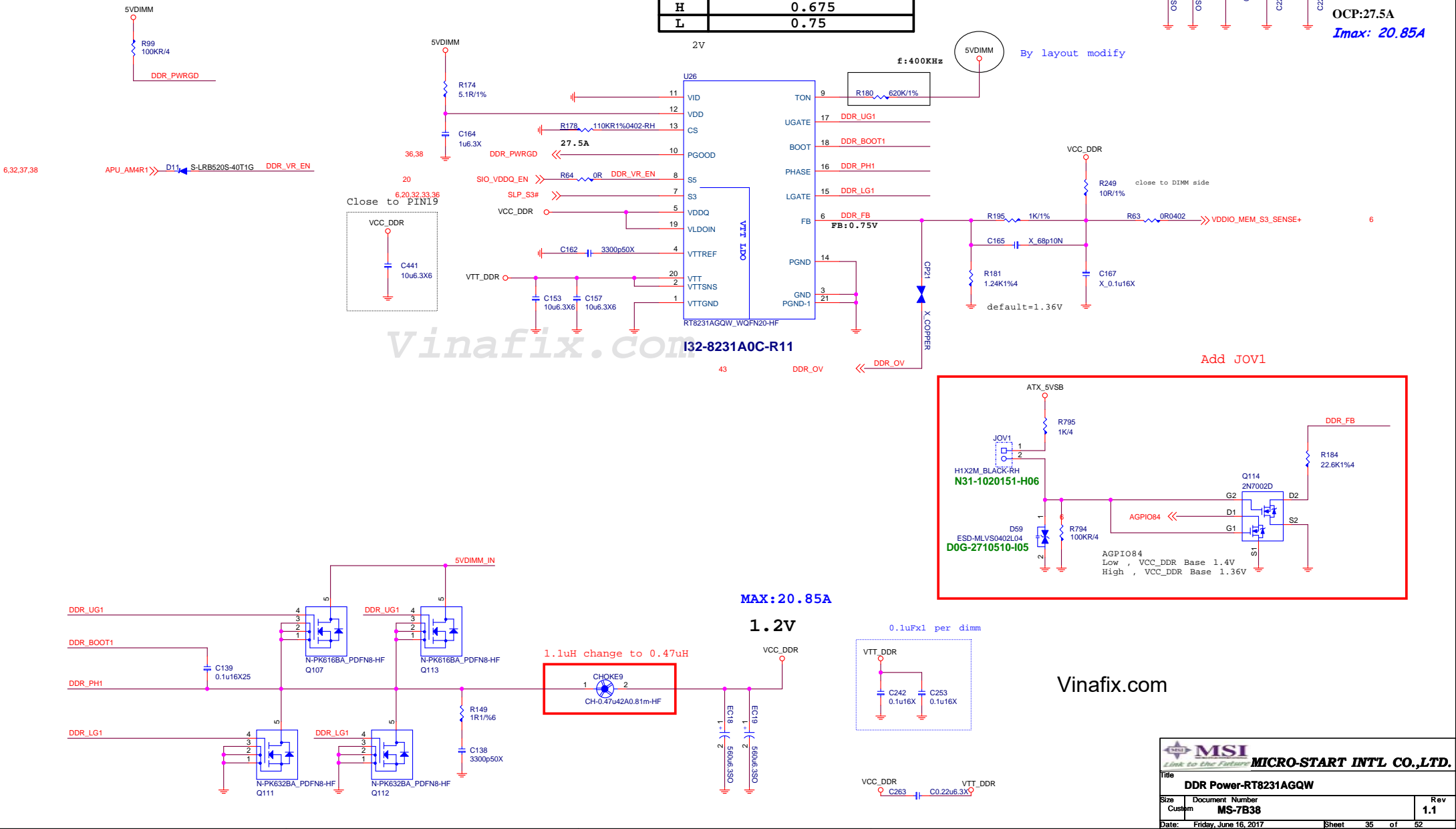
4.75A FOR 2DIMM

0.6A FOR DDR VTT

Irms = Iout \* SQRT[D/N- (D)^21]  
VCCDDR:  
D=Vout/Vin=1.2/5=0.24  
N=Phase number=1  
=20.85A\*SQRT(0.24-0.0576)  
=5.21A

VID	Reference Voltage (V)
H	0.675
L	0.75

OCP:27.5A  
I<sub>max</sub>: 20.85A



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FOR CPU 1.8V S5

0.5A

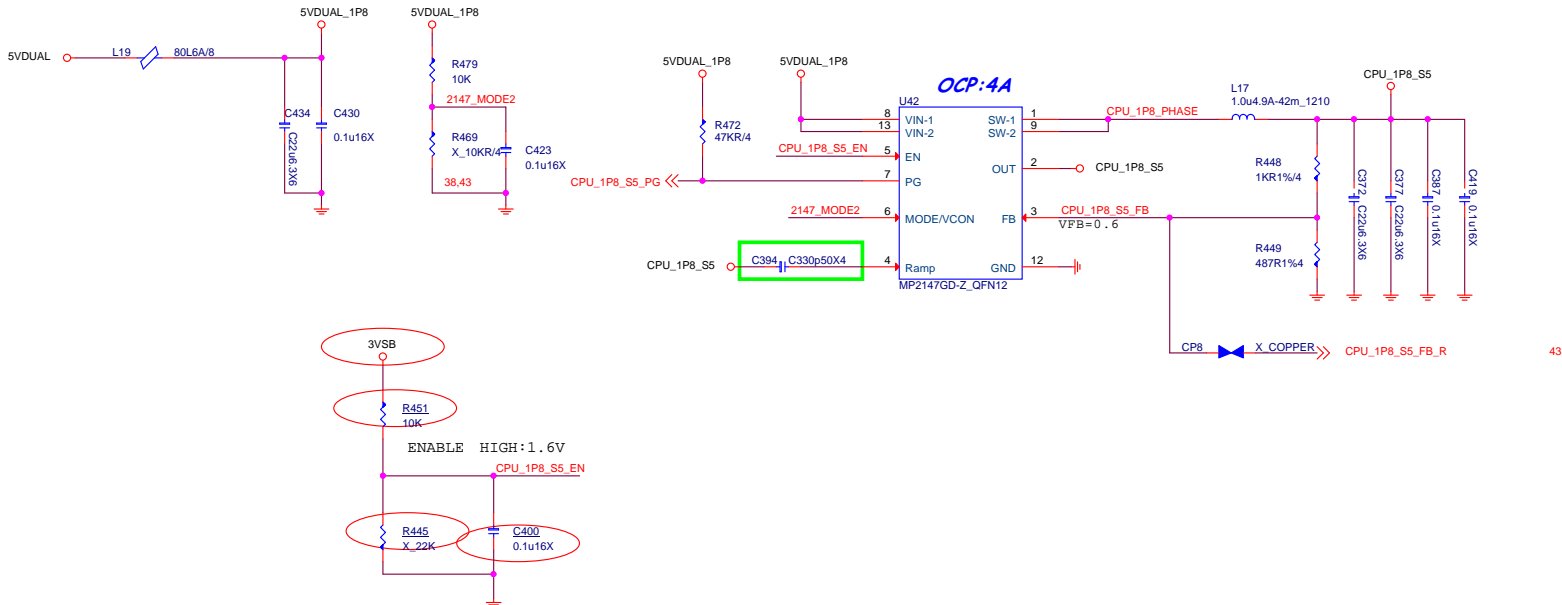
FOR VCCP\_SOC\_S5

0.9A

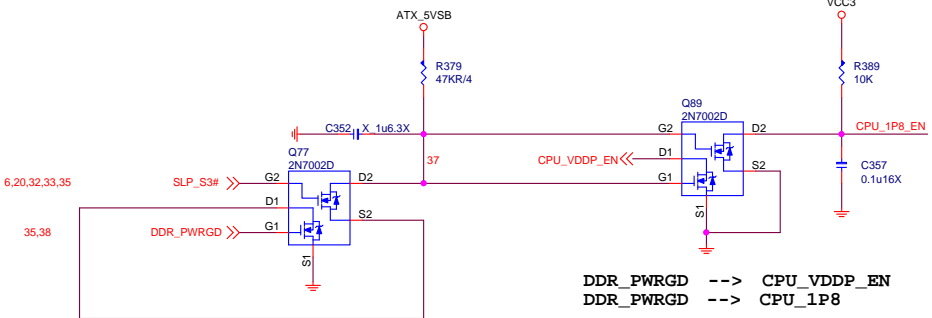
FOR CPU 1.8V S0

2.0A

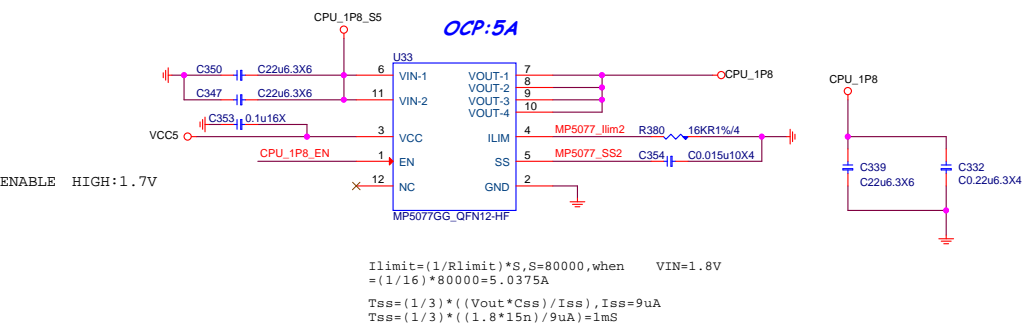
0.5A + 2.0A + 0.9A = 3.4A



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DDR\_PWRGD --> CPU\_VDDP\_EN  
DDR\_PWRGD --> CPU\_1P8

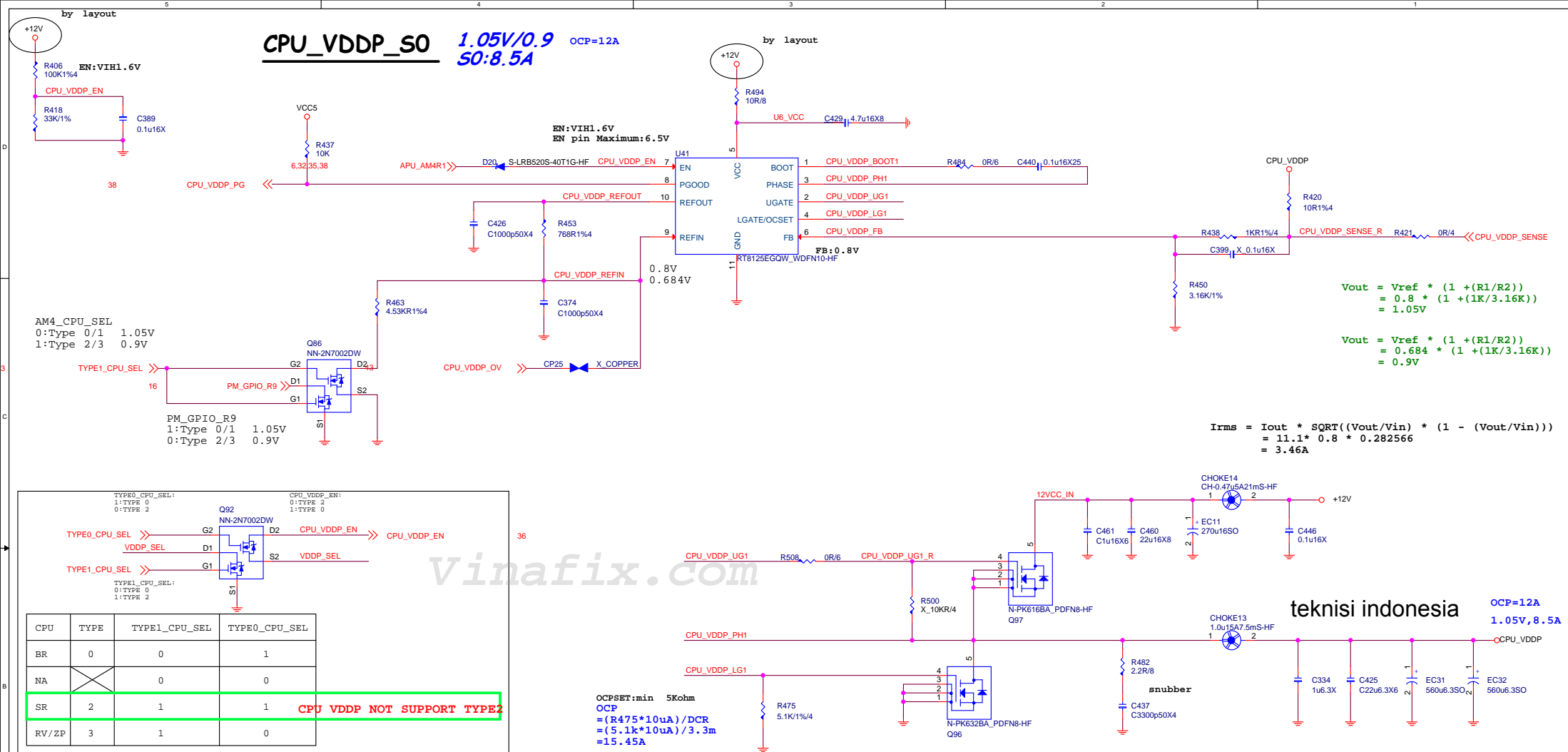


Ilimit=(1/Rlimit)\*S,S=80000,when VIN=1.8V  
=(1/16)\*80000=5.0375A  
Tss=(1/3)\*((Vout\*Css)/Iss),Iss=9uA  
Tss=(1/3)\*((1.8\*15n)/9uA)=1mS

# CPU\_VDDP\_S0

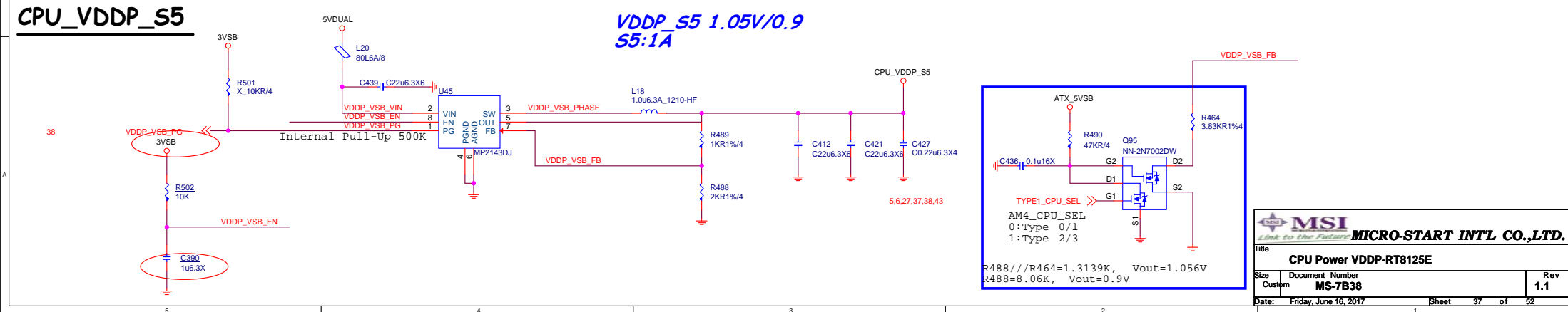
1.05V/0.9  
S0:8.5A

OCP=12A

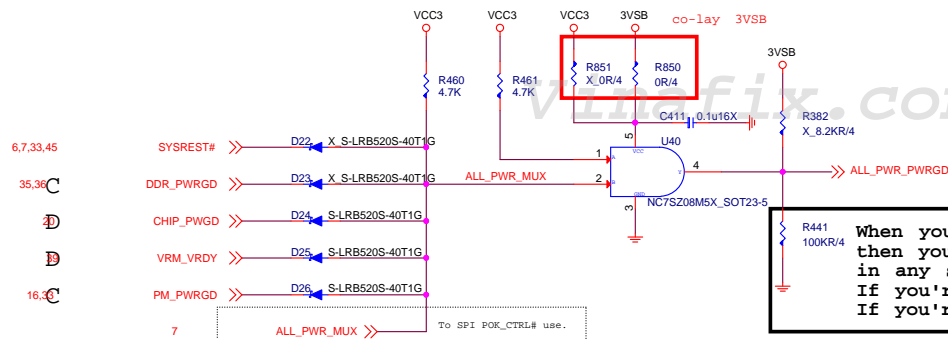


# CPU\_VDDP\_S5

VDDP\_S5 1.05V/0.9  
S5:1A

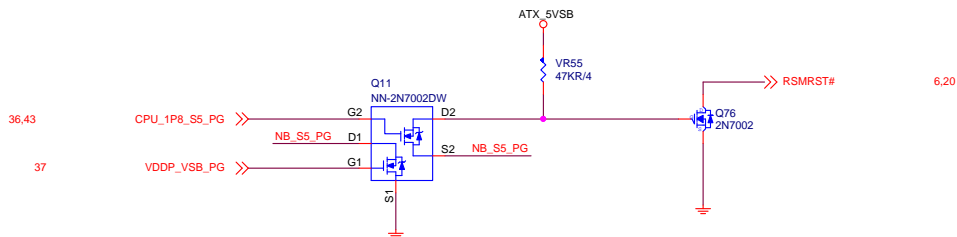


## ALL POWER GOOD MUX

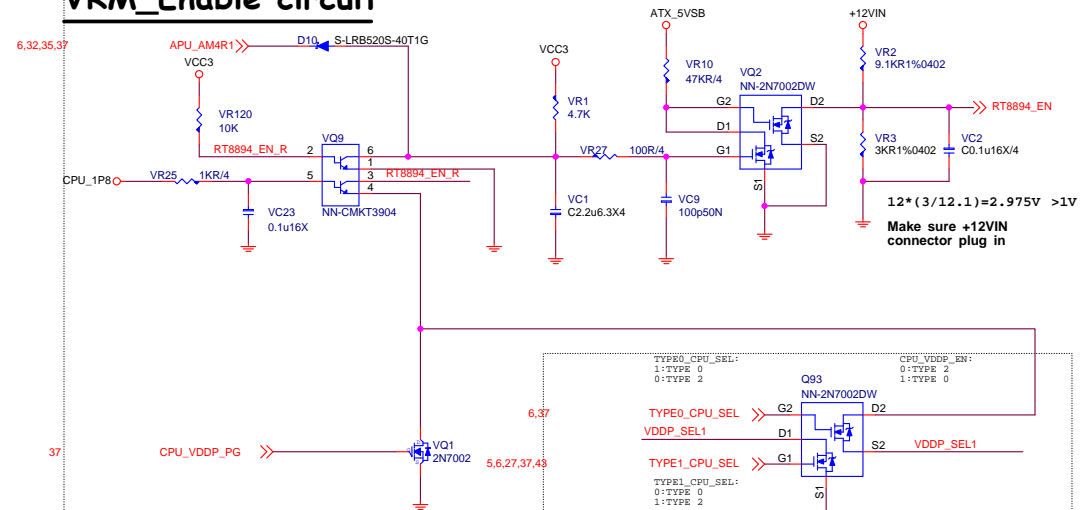


S0 PG

S5 PG



## VRM\_Enable circuit

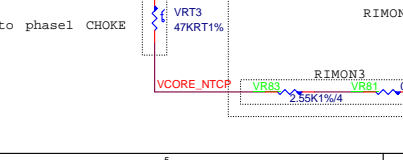
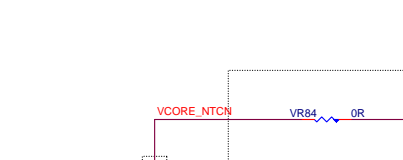
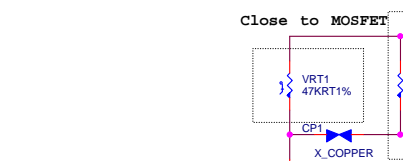
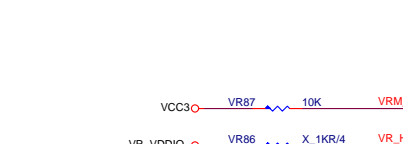
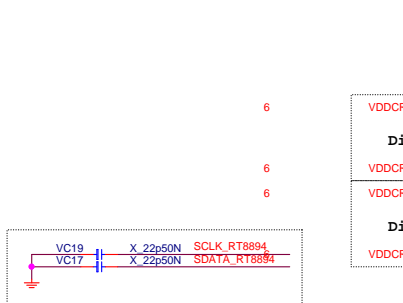
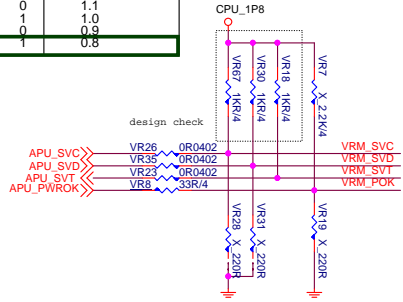


CPU VDDP NOT SUPPORT TYPE2

CPU	TYPE	TYPE1_CPU_SEL	TYPE0_CPU_SEL
BR	0	0	1
NA	X	0	0
SR	2	1	1
RV/ZP	3	1	0

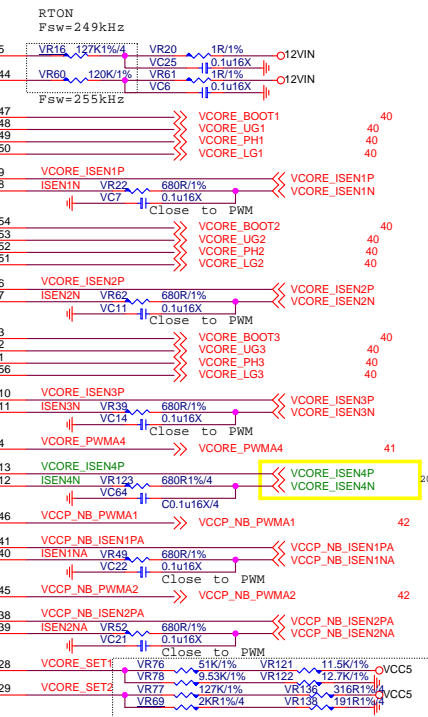
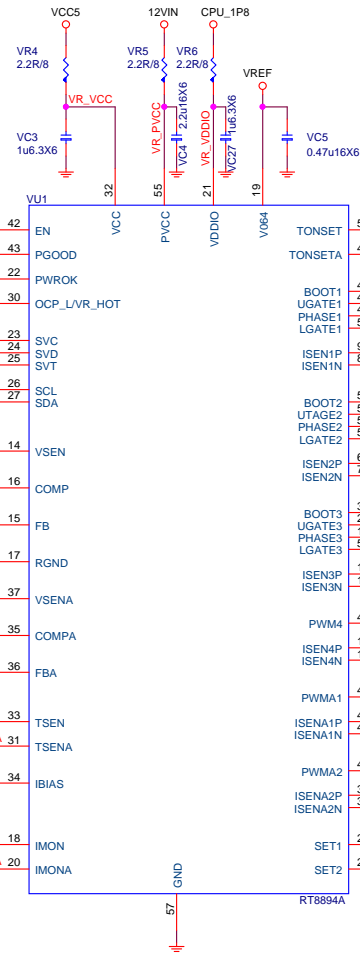
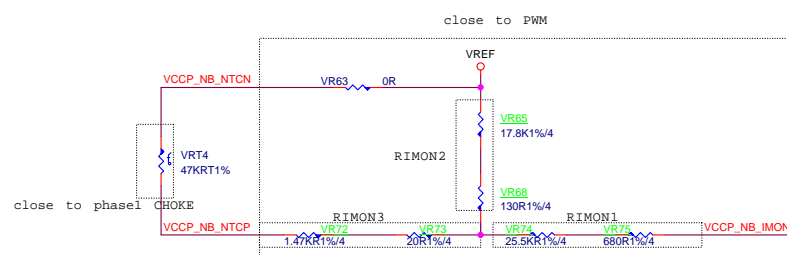
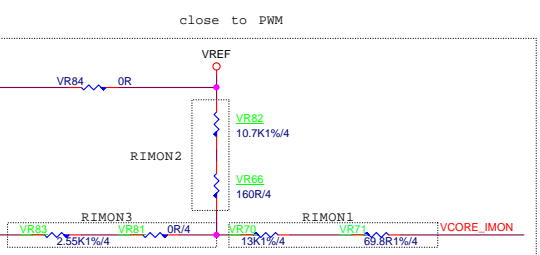
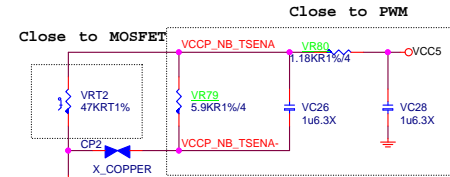
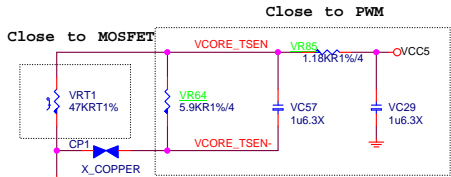
Note:VID Override Circuit

BOOT VOLTAGE		Pre_PWROK Metal VID
SVC	SVD	
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8



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VR\_HOT# pull low when T>110°C  
VR\_HOT# pull high when T drop to 90°C  
choose VRHOT\_Low=51\*VCC and VRHOT\_High=51\*VCC




SMB Address: 0X40

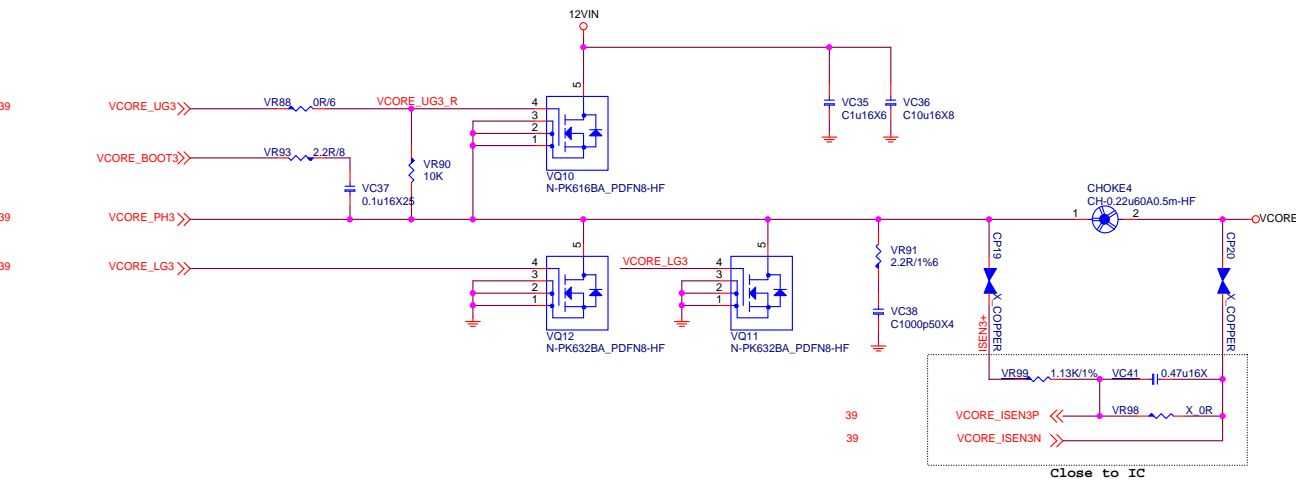
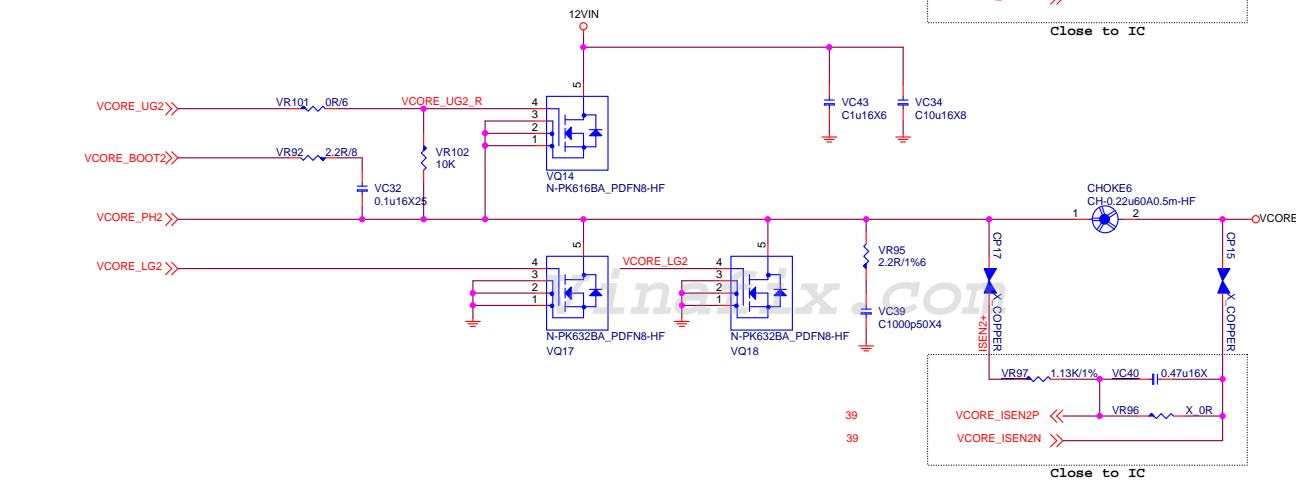
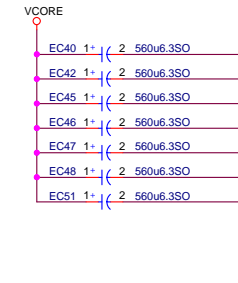
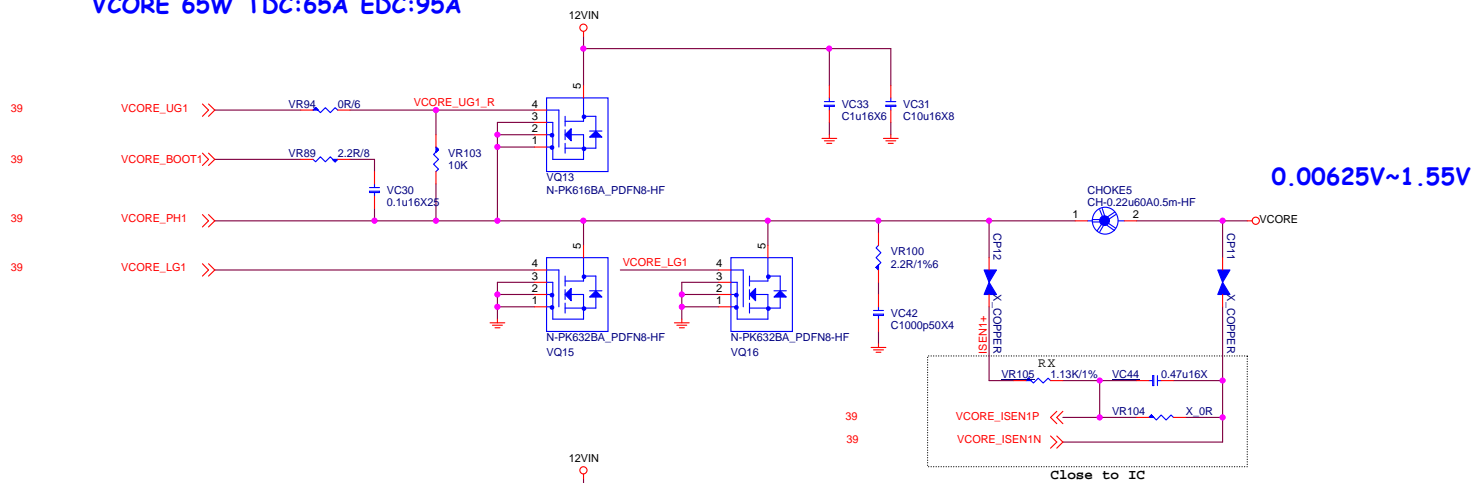
SET1 control ICCMAX, OCP setting  
SET2 control Internal compensation

VCORE IccMAX: 125A =>OCP=>140A  
VCC\_NB IccMAX: 75A =>OCP=> 95A

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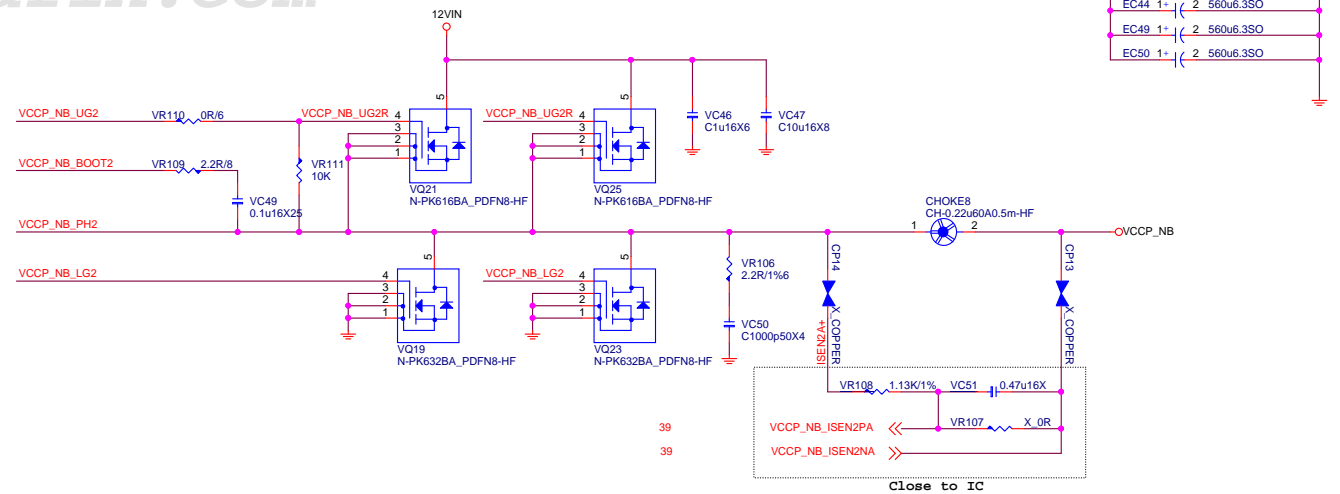
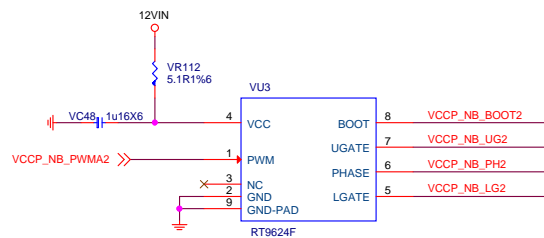
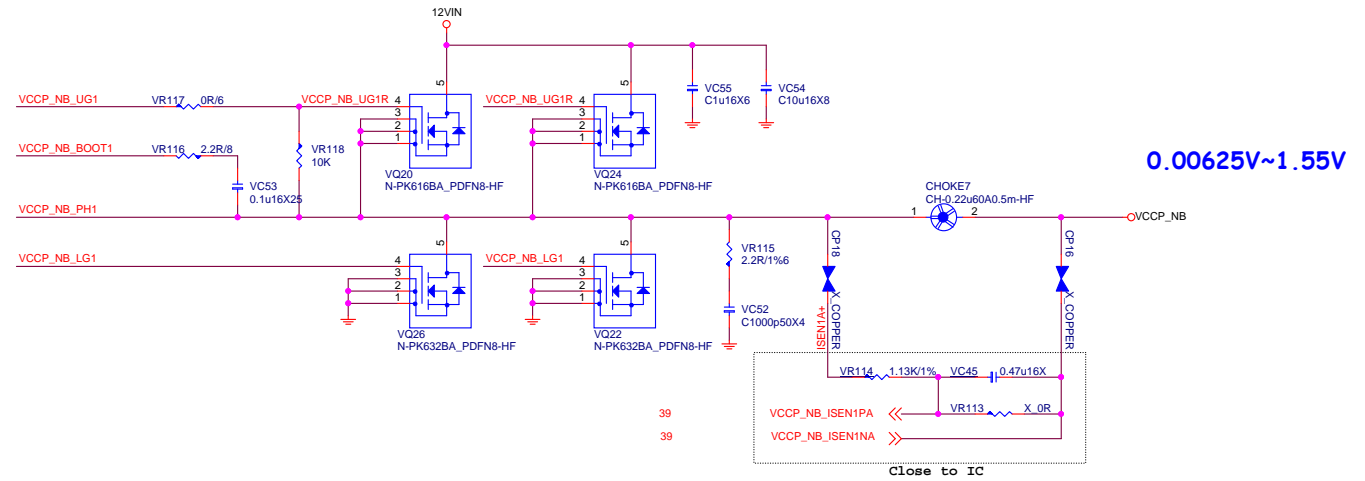
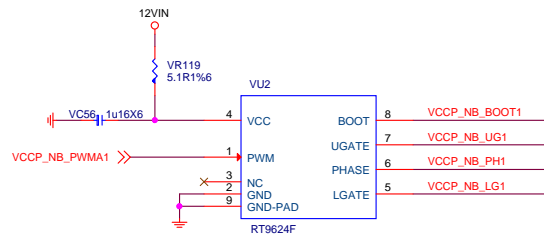
 <b>MSI</b> <i>Link to the Future</i>		<b>MICRO-START INT'L CO.,LTD.</b>	
Title			
<b>CPU Power RT8894 3+2 Phase</b>			
Size	Document Number	Rev	
Custom	<b>MS-7B38</b>	1.1	
Date:	Friday, June 16, 2017	Sheet	39 of 52

VCORE 95W TDC:80A EDC:125A  
VCORE 65W TDC:65A EDC:95A





VCCP\_NB 95W TDC:50A EDC:75A  
VCCP\_NB 65W TDC:50A EDC:75A





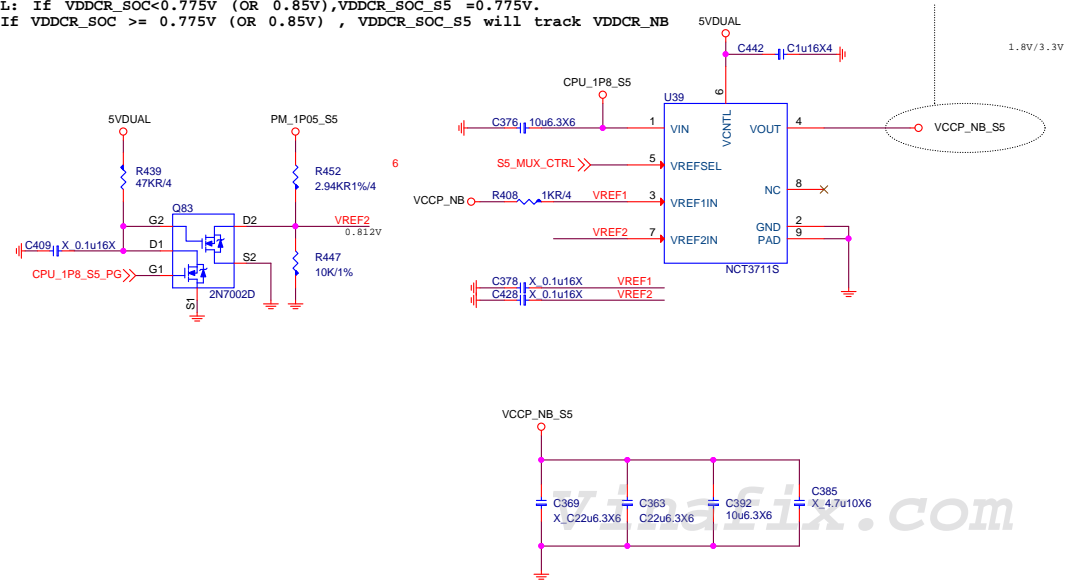
FOR VCCP\_SOC\_S5  
0.9A

TYPE0 Only

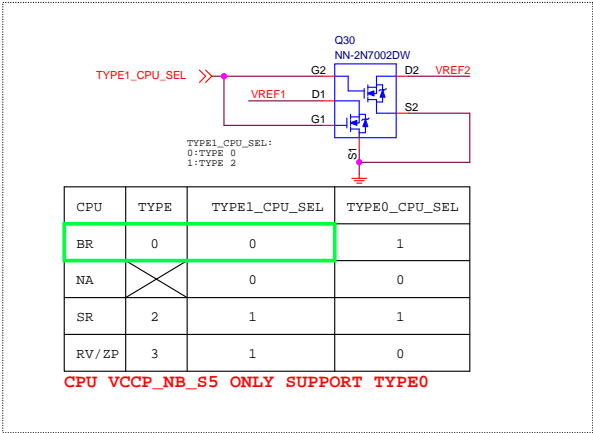
S5\_MUX\_CTRL  
HIGH:S0  
LOW: S3/S5

H: +VDDCR\_FCH\_ALW will track VDDNB  
L: If VDDCR\_SOC<0.775V (OR 0.85V),VDDCR\_SOC\_S5 =0.775V.  
If VDDCR\_SOC >= 0.775V (OR 0.85V) , VDDCR\_SOC\_S5 will track VDDCR\_NB

(VDDCR\_SOC\_S5 is only used for AMD Family 15h Models 60h-6Fh processors)Bristol Ridge TYPE0

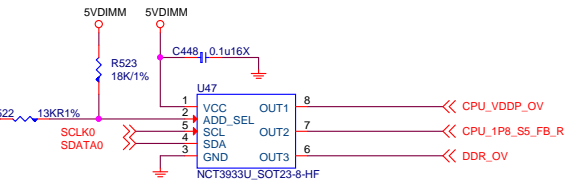


5,6,27,38



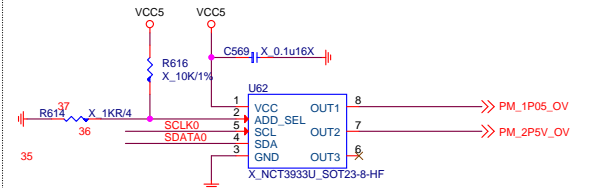
Over Voltage Control IC

0x26:RH=18K,RL=13K

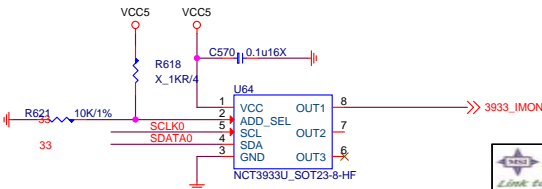


除設置參數外 有任何幫助,否則不 上N CT3933 與開 起過壓 項

0x20:RH=10K,RL=OPEN




0x2A:RH=OPEN,RL=10K



UPI VOLTAGE CONSOLE

ADDRESS	0x2A	0x28	0x26	0x24	0x22	0x20
RH (KOhm)	OPEN	3.9	3	2.2	1.3	10
RL (KOhm)	10	1.3	2.3	3	3.9	OPEN
BUS_SEL	0%	25%	40%	60%	75%	100%

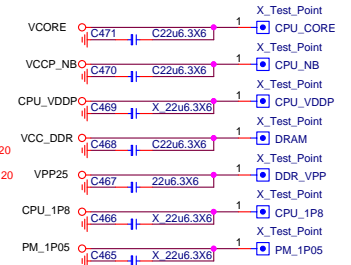
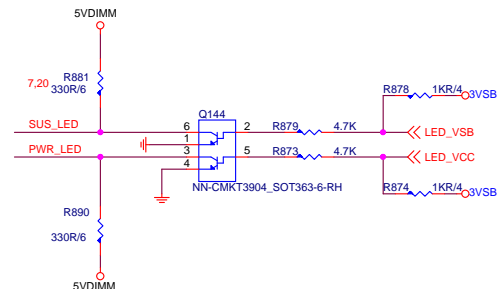
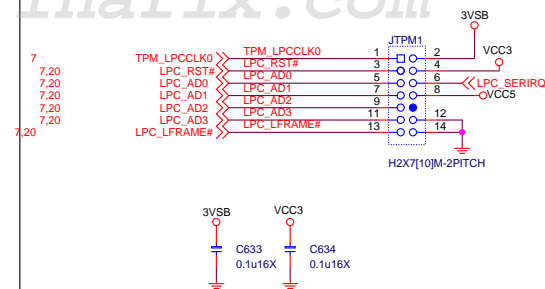
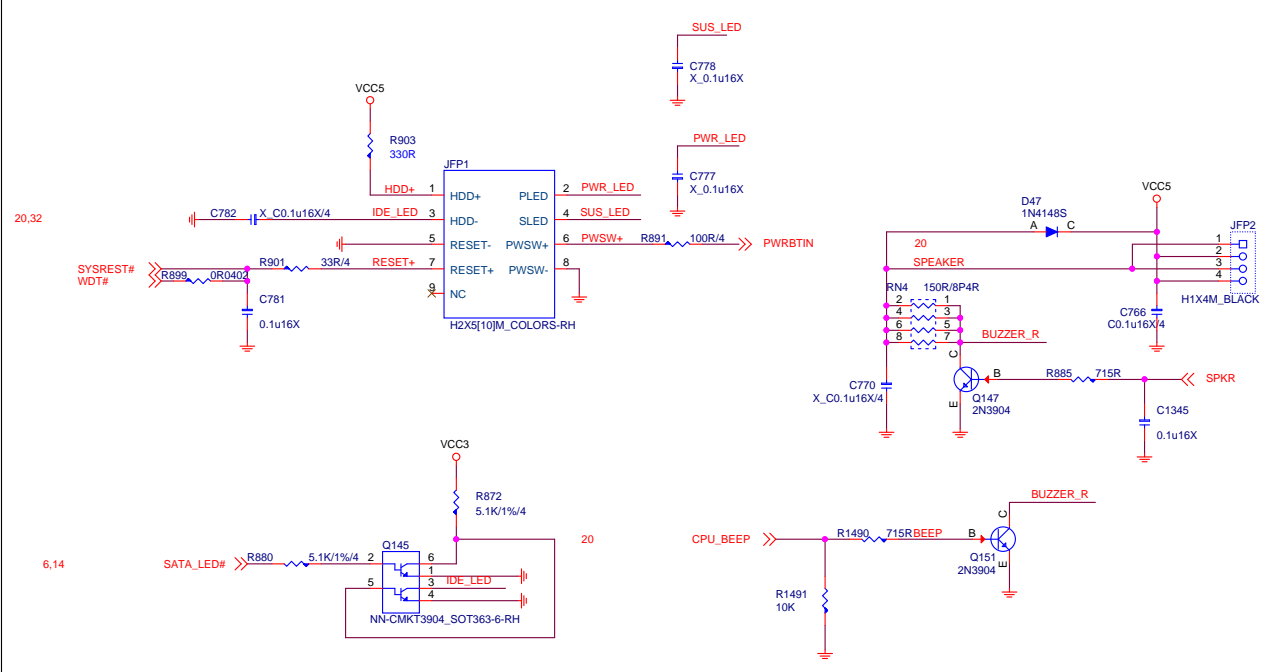
**MICRO-START INTL CO.,LTD.**

Title  
**CPU Power NB Switch / NCT3933 OV**

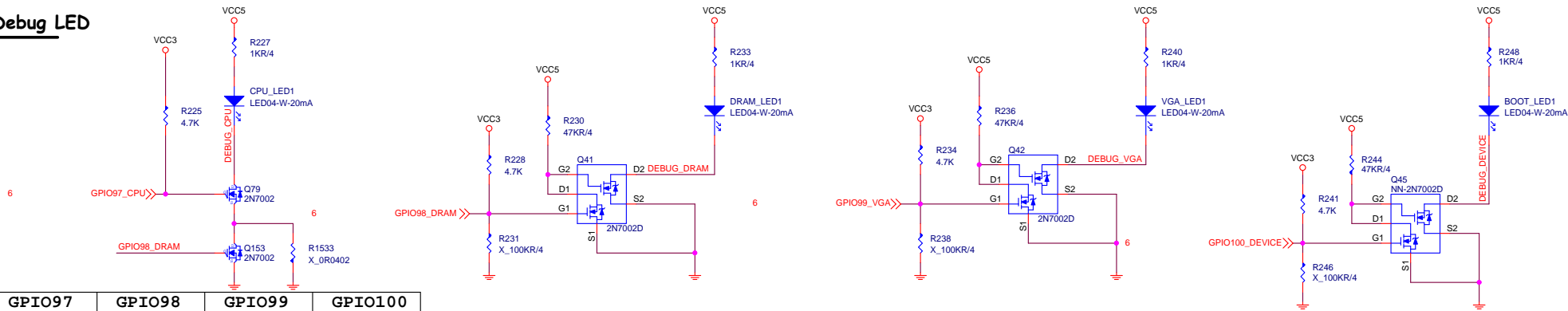
Size Custom	Document Number <b>MS-7B38</b>	Rev <b>1.1</b>
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Date: Friday, June 16, 2017 Sheet 43 of 52



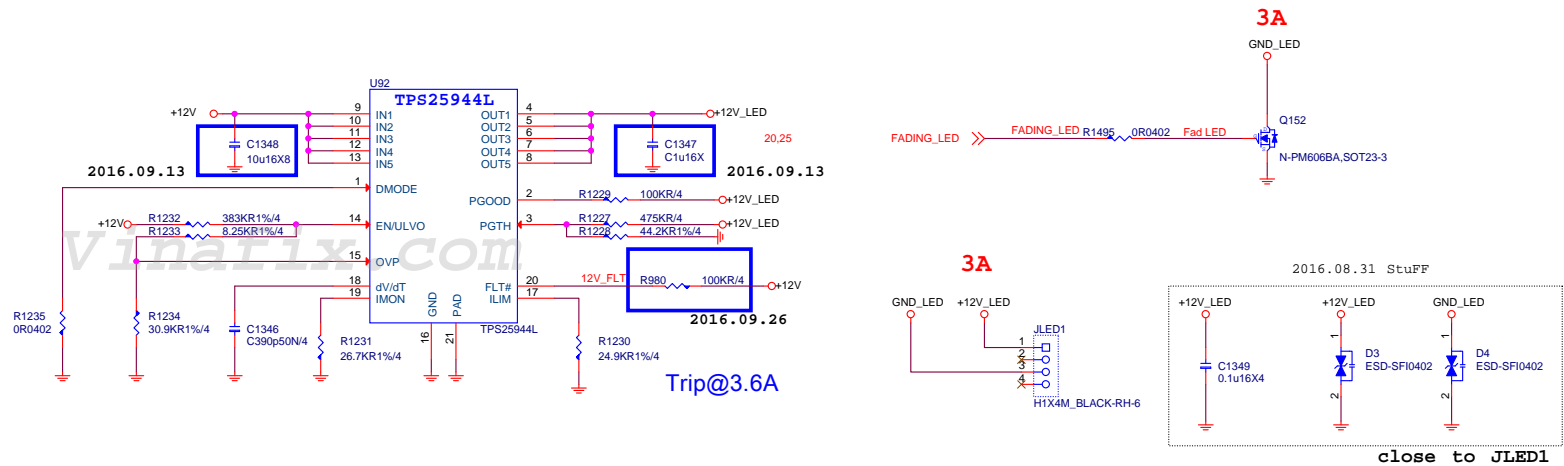


## EZ Debug LED

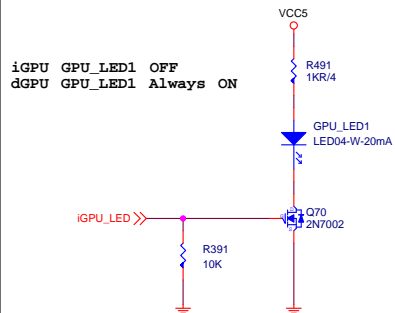


GPIO	GPIO97	GPIO98	GPIO99	GPIO100
LED	GPI PULL HIGH	GPO PO LOW	GPO PO LOW	GPO PO LOW
亮	GPI PULL HIGH	GPO PO LOW	GPO PO LOW	GPO PO LOW
滅	GPO LOW	GPO HIGH (default HIGH)	GPO HIGH (default HIGH)	GPO HIGH (default HIGH)

## JLED



### AM4 APU Detect LED Circuit



### Bottom LED

LED	x16	x8	x4
PCIE2	Red	White	White

GPIO	EGPIO95	EGPIO96
LED	EGPIO95	EGPIO96
亮	GPO PO HIGH	GPO PO HIGH
滅	GPI (default LOW)	GPI (default LOW)

MSI  
Link to the Future  
MICRO-START INTL CO.,LTD.

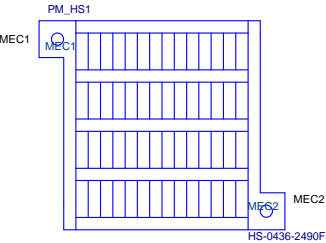
Title: ALL LED Control

Size: Custom  
Document Number: MS-7B38  
Date: Friday, June 16, 2017

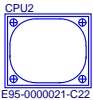
Rev: 1.1

Sheet 46 of 52

HEAT SINK

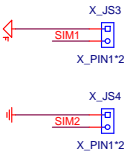


CPU Socket

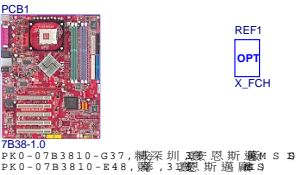


RETENTION MODULE

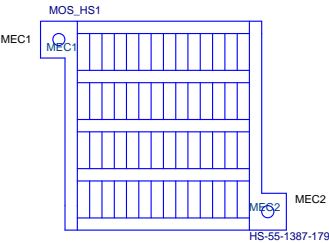
Simulation



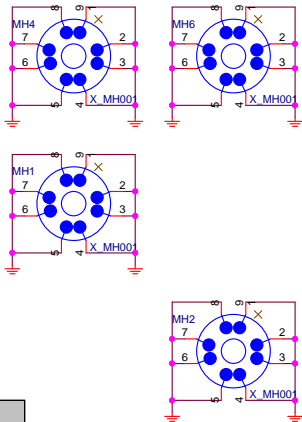
MANUAL PART



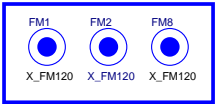
MOS HS(VCORE)



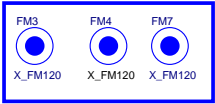
Optics Orientation Holes




5010



5020



OPT	Configure	BOM	Function
		601-7A36-A01	XXXX
		601-7A36-01S	MS-7A36 1.0 A320 A320M PRO-VD,A320,AM4,2DDR4,1PCI-Ex16,2PCI-Ex1,4SATA3,6USB3.1 Gen1,HD Audio,GB LAN,DVI-D,VGA
		601-7A36-010	MS-7A36 1.0 A320 A320M PRO-VD,A320,AM4,2DDR4,1PCI-Ex16,2PCI-Ex1,4SATA3,6USB3.1 Gen1,HD Audio,GB LAN,DVI-D,VGA



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Title		
BOM OPTION		
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